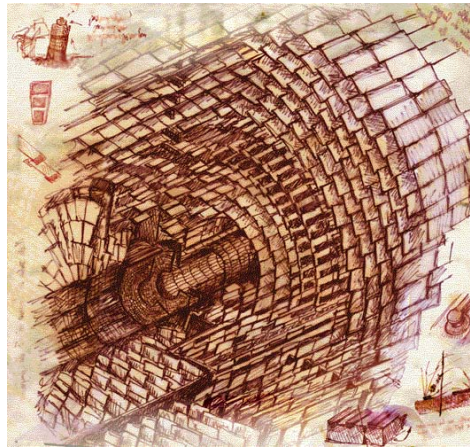


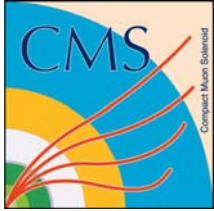
# *The CMS Silicon Strip Tracker experience*

*Gabriella Pásztor*

*University of California, Riverside*



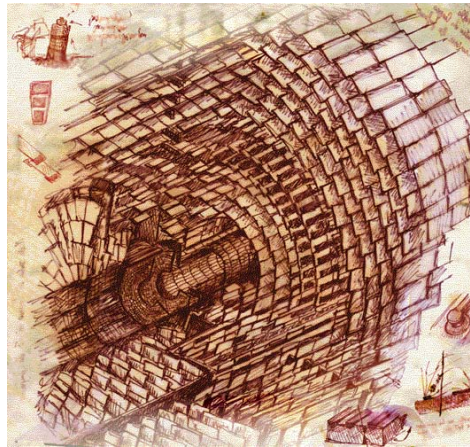
*ECFA ILC Workshop, SiLC Meeting  
Vienna, 18 November, 2005*



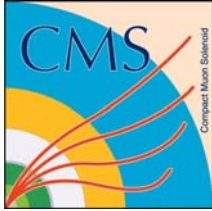
# *The CMS Silicon Strip Tracker experience ...with some emphasis on problems*

*Gabriella Pásztor*

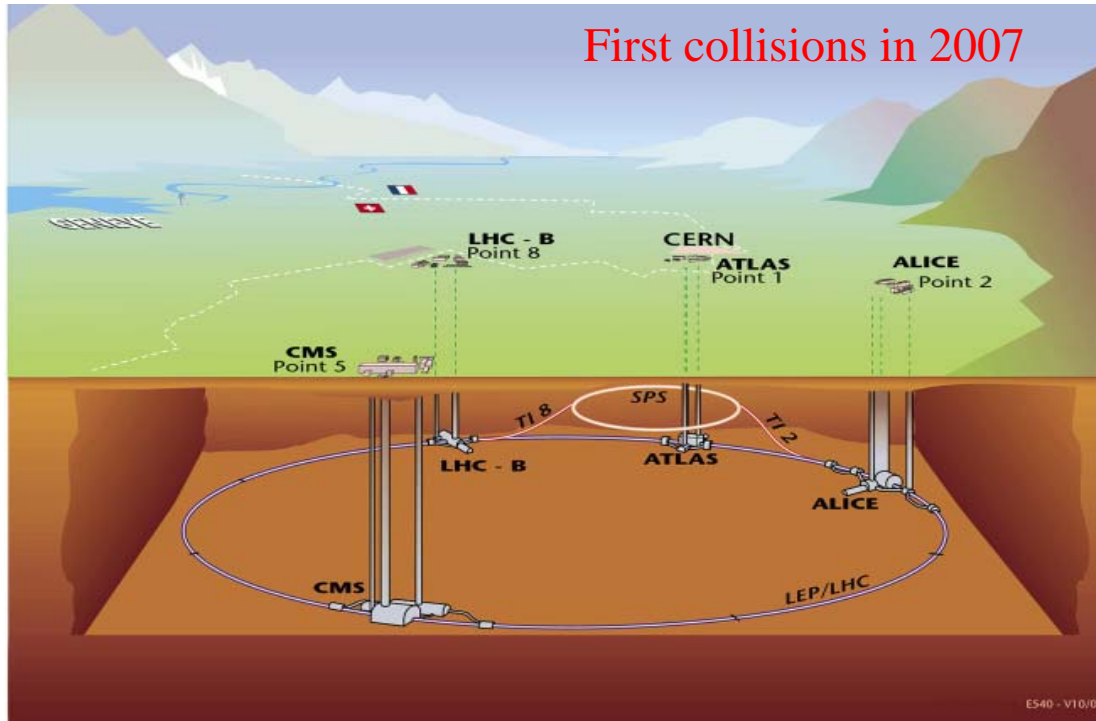
*University of California, Riverside*



*ECFA ILC Workshop, SiLC Meeting  
Vienna, 18 November, 2005*

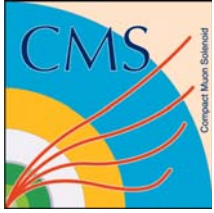


# The LHC machine



LHC beam	Energy (TeV)	Luminosity ( $cm^{-2} s^{-1}$ )
$pp$	14	$10^{34}$
$Pb Pb$	1312 (5.5/N)	$10^{27}$

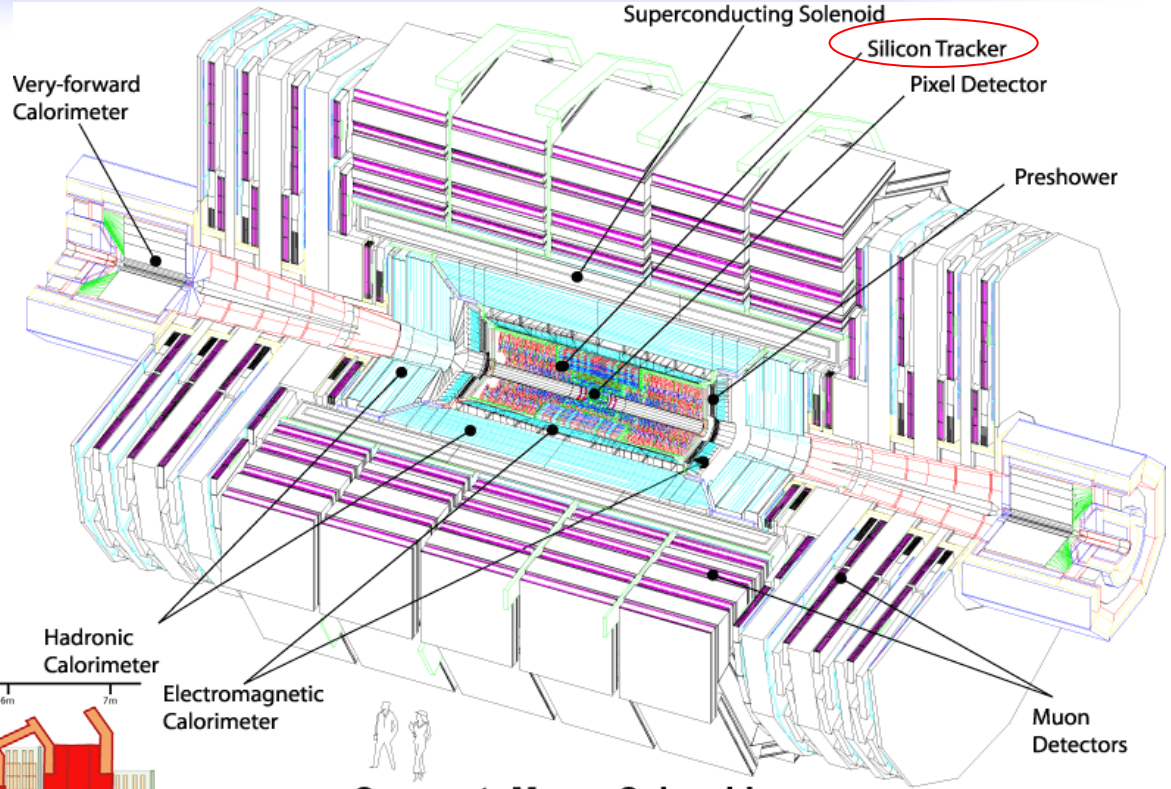
- inelastic cross-section:  $\sim 70$  mb
- interactions/bunch:  $\sim 20$
- tracks / unit rapidity:  $\sim 140$
- charged tracks /  $cm^2$  :  
1 @ 10 cm from IP  
0.1 @ 25 cm from IP  
0.01 @ 60 cm from IP
- L1 trigger delay: 3.2  $\mu s$
- mean L1 trigger rate:  $< 100$  kHz
- beam diameter: 20  $\mu m$
- bunch length: 75 mm
- protons/bunch:  $10^{11}$
- bunches/beam: 2835
- beam crossing rate: 40 MHz
- annual  $\int L$ :  $5 \cdot 10^{40} cm^{-2}$



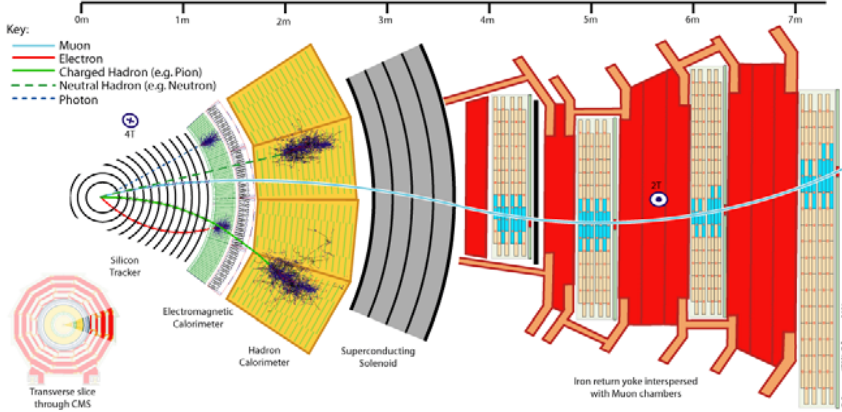
# The CMS detector

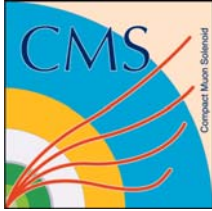
Compact  
Muon  
Solenoid  
detector

Magnetic field: 4 T  
Weight: 12500 t  
Length: 21.6 m  
Diameter: 15 m



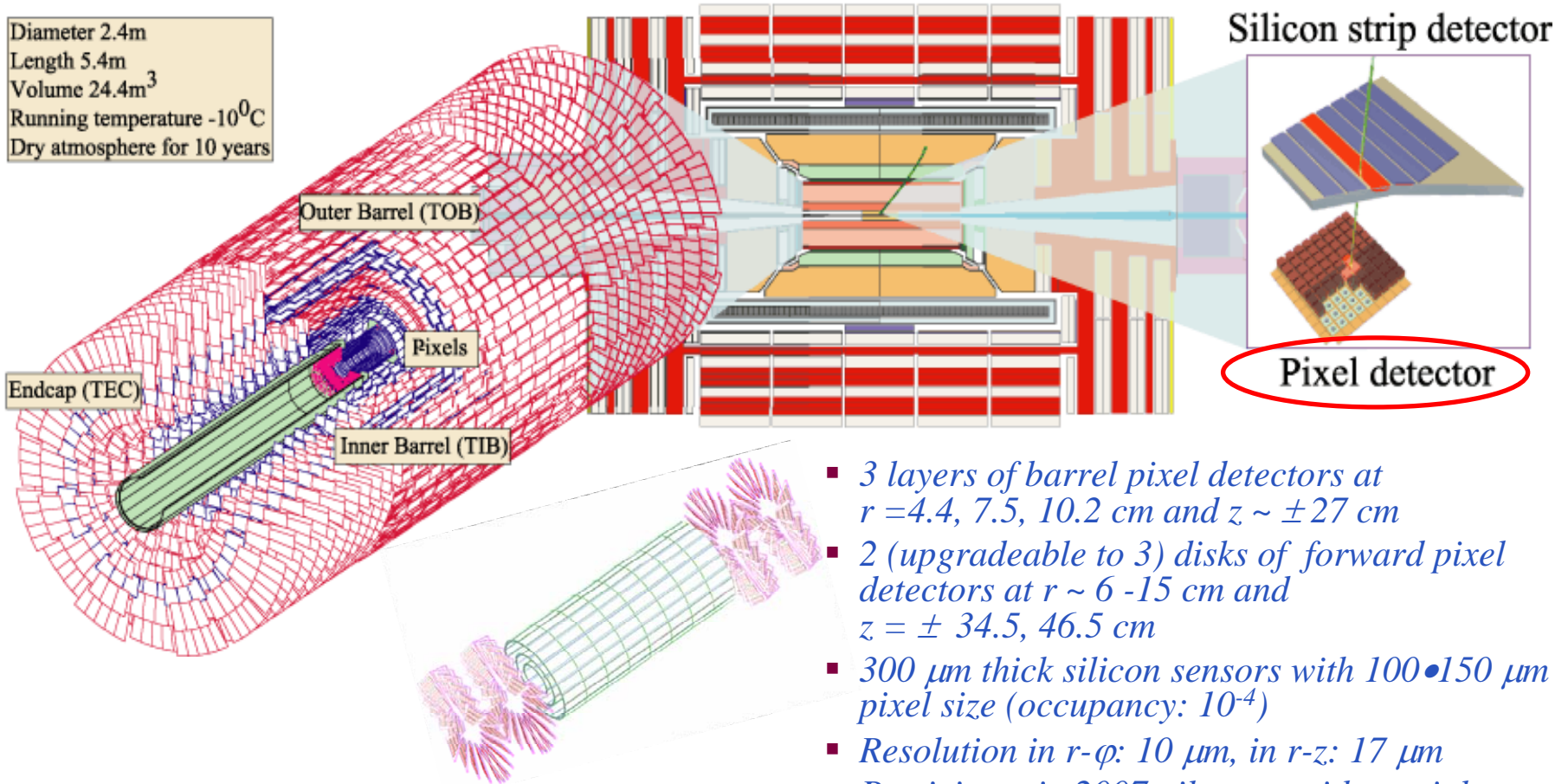
Compact Muon Solenoid



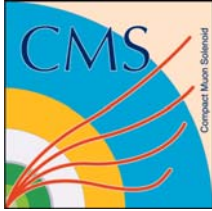


# The Silicon Tracker

Diameter 2.4m  
 Length 5.4m  
 Volume 24.4m<sup>3</sup>  
 Running temperature -10<sup>0</sup>C  
 Dry atmosphere for 10 years



- 3 layers of barrel pixel detectors at  $r = 4.4, 7.5, 10.2$  cm and  $z \sim \pm 27$  cm
- 2 (upgradeable to 3) disks of forward pixel detectors at  $r \sim 6 - 15$  cm and  $z = \pm 34.5, 46.5$  cm
- 300  $\mu\text{m}$  thick silicon sensors with  $100 \bullet 150 \mu\text{m}$  pixel size (occupancy:  $10^{-4}$ )
- Resolution in  $r-\phi$ : 10  $\mu\text{m}$ , in  $r-z$ : 17  $\mu\text{m}$
- Participate in 2007 pilot run with partial detector, install for 2008 physics run



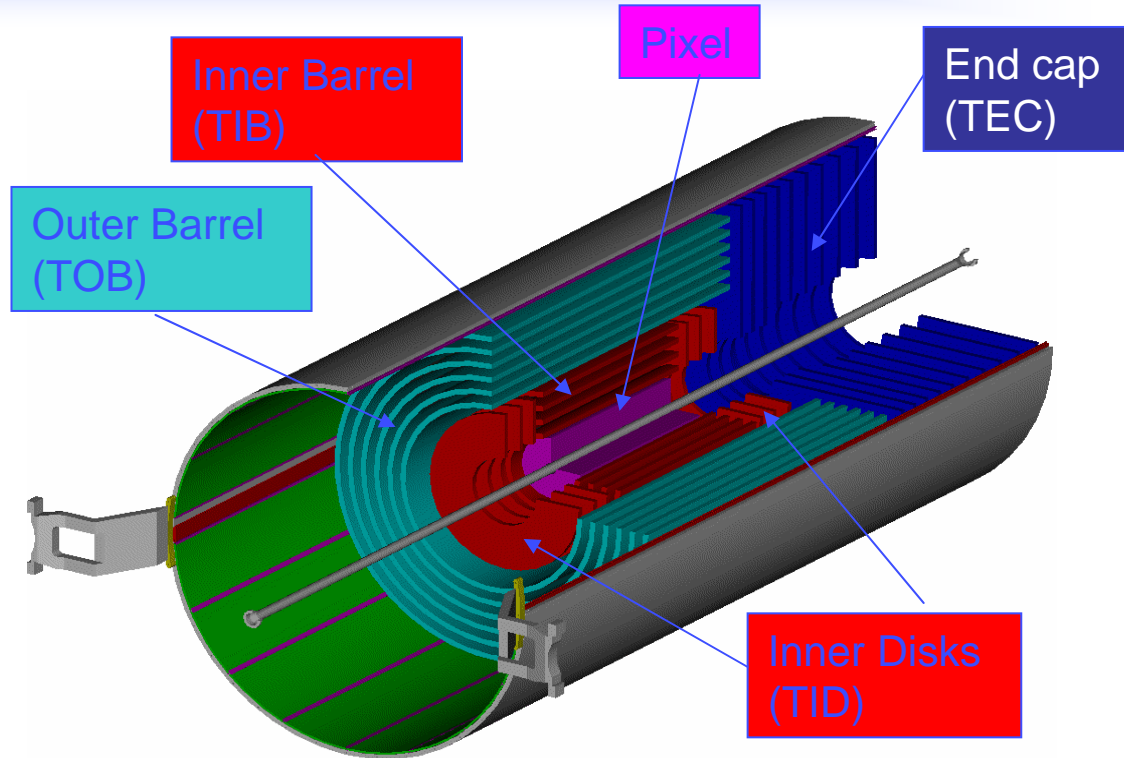
# The Silicon Tracker

15 148 modules  
24 244 silicon sensors  
210 m<sup>2</sup> active silicon area  
75 000 readout chips  
9 600 000 readout channels  
(=silicon strips)  
25 000 000 wire bonds

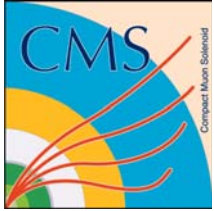
37 000 analog optical links  
3000 km optical fibres  
440 Front-End Drivers

29 module designs  
16 sensor designs  
12 hybrid designs

Fluences in  $n(1 \text{ MeV equiv..})/\text{cm}^2$ :  
Inner region:  $\leq 1.6 \cdot 10^{14}$   
Outer region:  $\leq 3.5 \cdot 10^{13}$



Length: 5.4 m      Diameter: 2.4 m  
Volume: 24.4 m<sup>3</sup>      Weight: 3 t  
Running temperature: below -10 C  
Dry (<30 RH%) atmosphere (inert nitrogen)  
Power dissipation: 45 kW

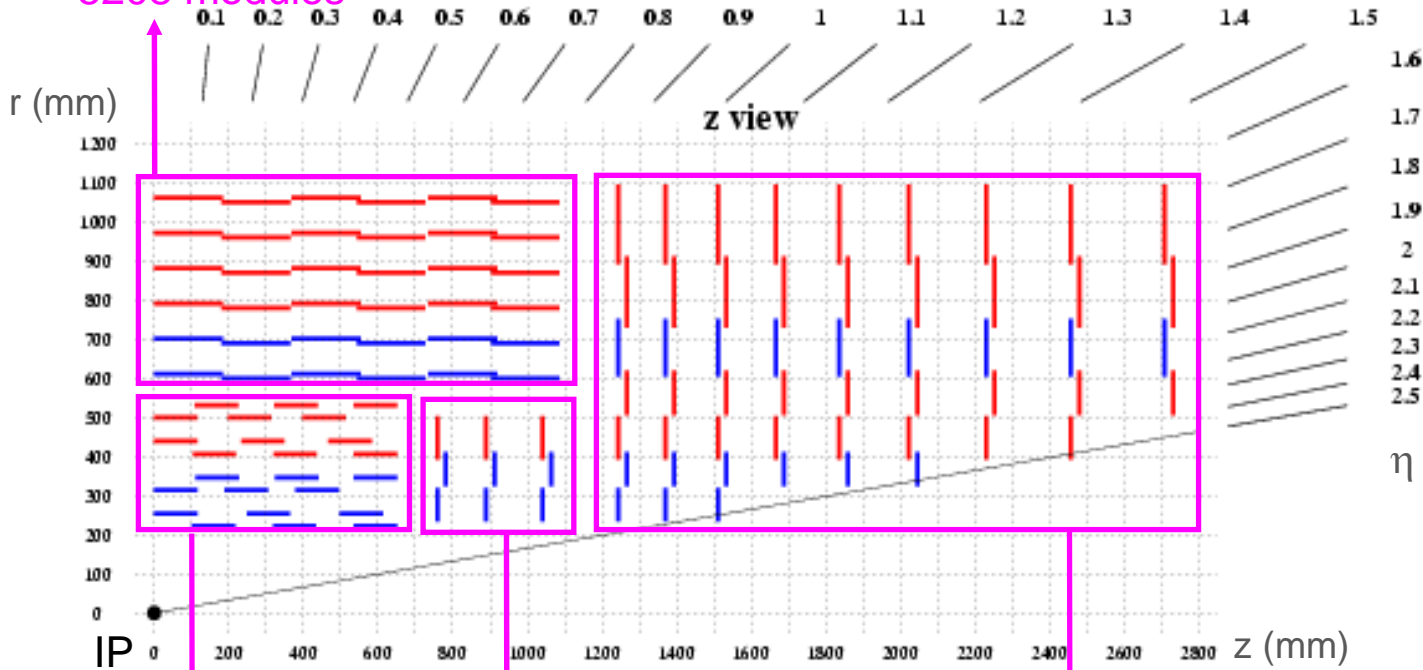


# The Silicon Strip Tracker layout

**TOB**  
6 layers  
5208 modules

Single sided

Double sided  
(100 mrad stereo angle)

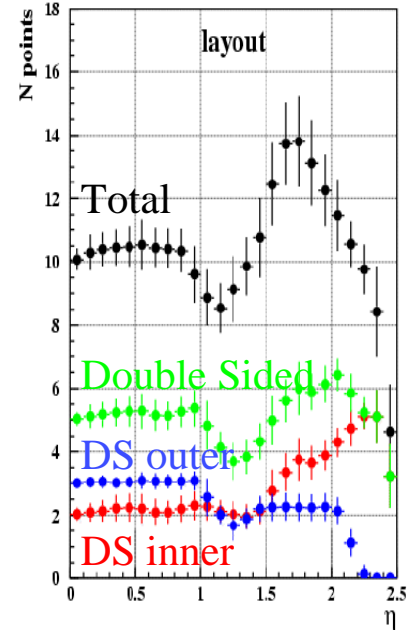


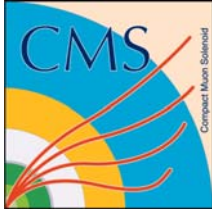
**TIB**  
4 layers  
2724 modules

**TID**  
2x3 disks  
816 modules

**TEC**  
2x9 disks  
6400 modules

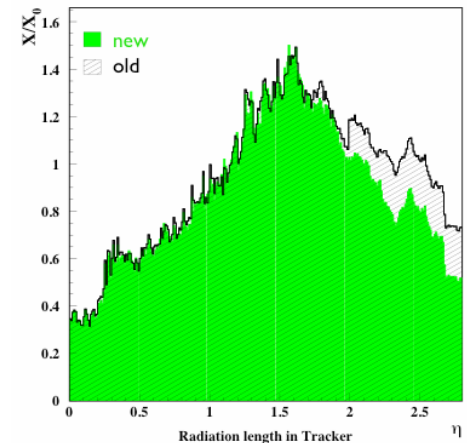
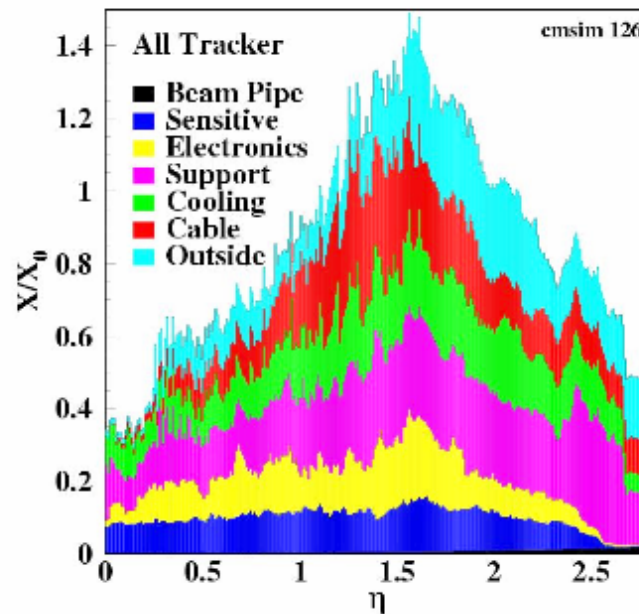
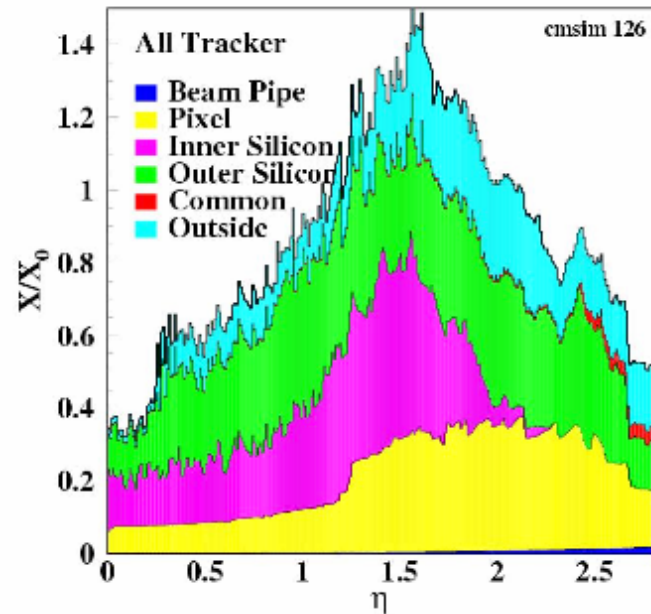
Barrel: strips parallel to beam  
End cap: strips in radial direction



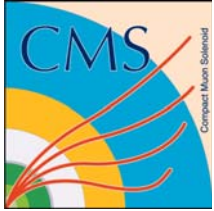


# Material budget

- *Design goal in 1999:  $X/X_0 < 1$*
- *Most of the material is electronics related (electronics, cooling, cable, ...)*
- *New description (with most importantly a better description of the bulkheads collecting the services behind the endcaps) predicts less material in the forward region*







# The basic element: the module

Support frame  
(graphite and carbon fiber)

Kapton circuit

- bias voltage
- backplane isolation
- temperature sensor

Front-end hybrid  
with flex tail

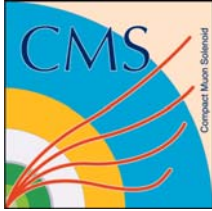
TEC ring 6 (r- $\phi$ ) module

Silicon sensors  
• 1 or 2 per module

micro-bonds

Pitch adapter (glass)  
• fan-out from readout  
chip to sensor

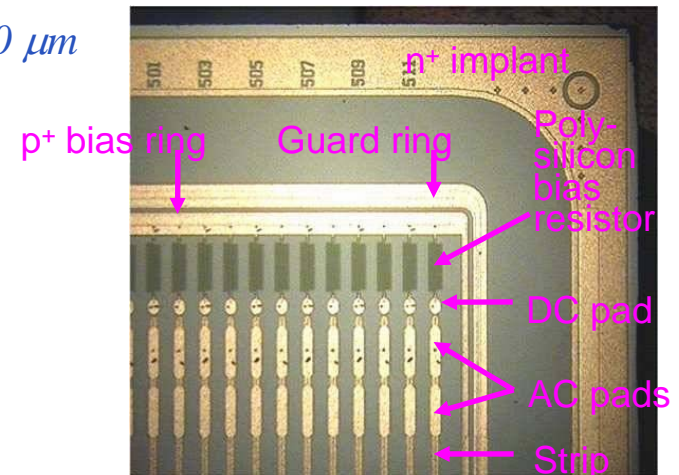
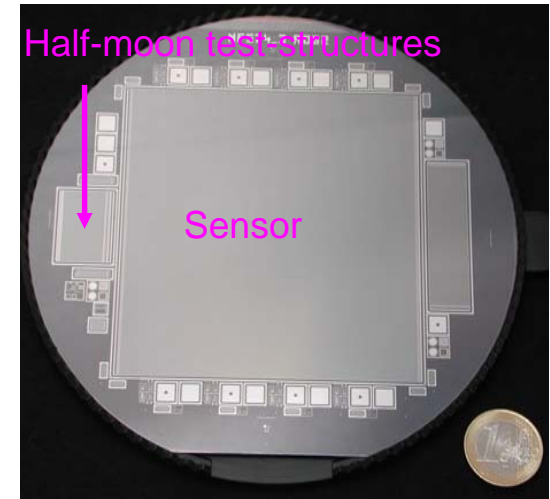
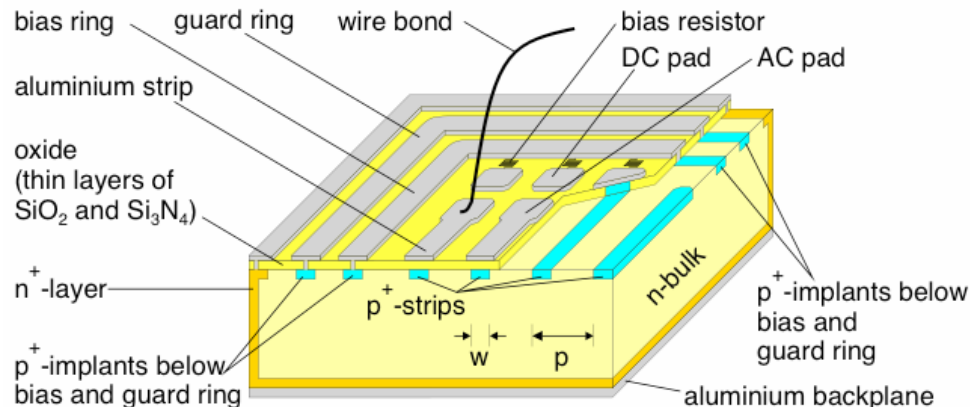
*Double sided modules are constructed from two independent single sided modules mounted back to back with a stereo angle of 100 mrad*

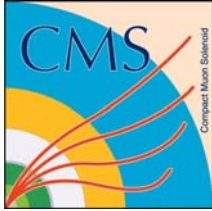


# Silicon strip sensors

*Simple and robust design, compatible with mass production*

- 16 different sensor designs, all single sided
- Rectangular or wedge-shaped sensors with typical size of 10x10 cm made on 6" wafer
- n-type bulk, not oxygenated, <100> lattice orientation  
Inner region: low resistivity 1.5-3.5 kΩcm, thin 320 μm  
Outer region: high resistivity 3.5-7.5 kΩcm, thick 500 μm
- 512 or 768 p<sup>+</sup> strip implants with pitch = 80-205 μm, width/pitch = 0.25
- Al readout strips, AC coupled, metal overhang 4-8 μm
- p<sup>+</sup> guard ring floating, metal field planes extend beyond implantation of guard ring
- Distance between the implant region and edge: 2·thickness+110 μm
- Identical test-structures on every wafer type



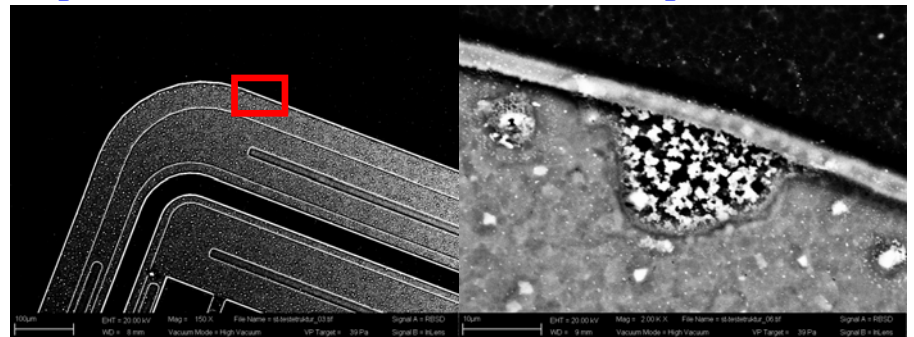


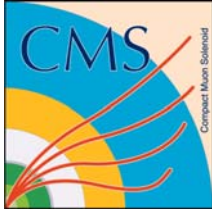
# Sensor production

- *Thin sensors: Hamamatsu Photonics (HPK)*
- *Problems with thick sensors from SGS Thomson Microelectronics (STM) during mass production (low yield, unstable/high leakage current, mechanical stress dependence, high common mode, high flatband voltage, low interstrip resistance, scratches, Al corrosion...) → bulk of thick sensor production shifted to HPK*
- *An unprecedented extensive study of delivered wafers: 16 electrical parameters measured on about 13000 structures (8673 sensors and 4144 test-structures) by the end of September 2005*

## Aluminium corrosion of ST sensors

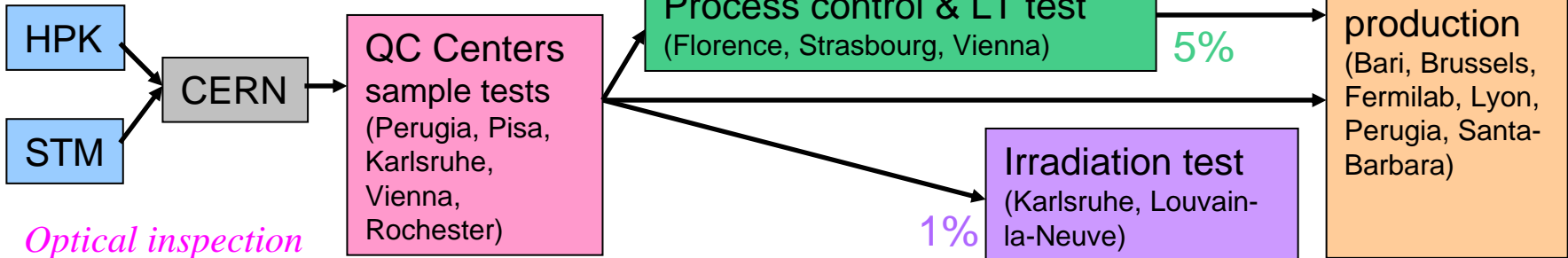
- *Development of dots and stains on the Al surfaces of STM sensors*
- *Electro-chemical corroding process: increase with time only when sensor is biased in the presence of humidity (>30%r.h.), leading to deep local micro-corrosions of Al surface*
- *Probable explanation: Potassium (remaining from SiO<sub>2</sub> etching?) forms an acid (We suspected previously Phosphorus that is present in 4% concentration in passivation oxide)*
- *Probable (but not proven) effect on long-term behaviour by compromising the metal overhang design*
- *Main reason to cancel the 19000 sensor ST contract*





# Sensor Quality Assurance

Tests and corresponding acceptance criteria agreed with manufacturers to ensure 99% acceptance by CMS



## Optical inspection

## Electrical characterization

- IV (0-550 V reverse bias)
  - Leakage current
  - Breakdown voltage
- CV (0-350 V)
  - Sensor capacitance
  - Depletion voltage
  - Thickness
- Strip scan @ 400 V bias
  - Single strip leakage current
  - Poly-resistor value
  - Coupling capacitance value
  - Dielectric current at 10 V (pinholes)

## <1 hour / test structure

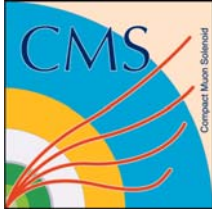
- IV on mini-sensor
- IV on gate-controlled diode
- CV on diode
- CV on MOS (flatband voltage)
- Interstrip capacitance
- Interstrip resistance
- Surface current
- p<sup>+</sup> implant resistivity
- Aluminum resistivity
- Poly-silicon resistivity
- Coupling capacitance
- Oxide breakdown voltage

26 MeV protons  
(Karlsruhe)

~20 MeV neutrons  
(Louvain)

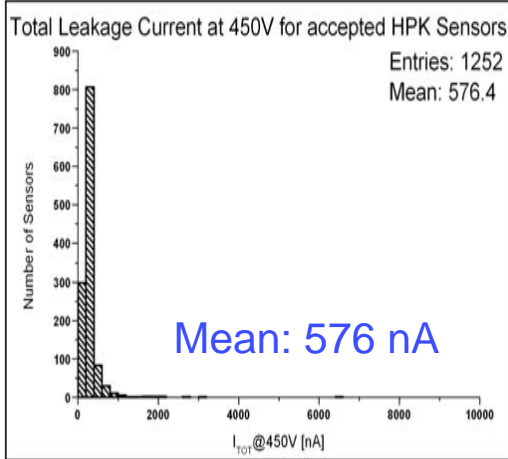
Measurements before and after irradiation:

- IV, leakage current
- CV, depletion voltage
- Strip parameters

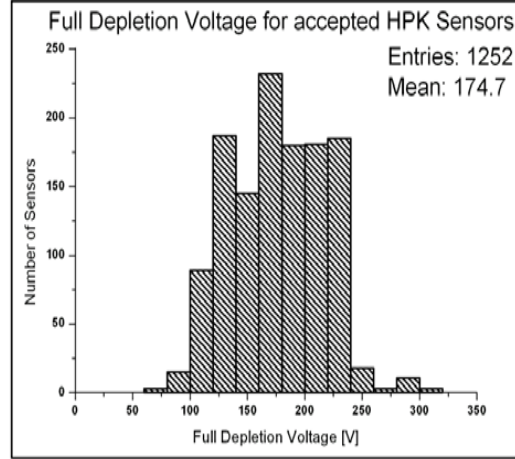


# Qualification results

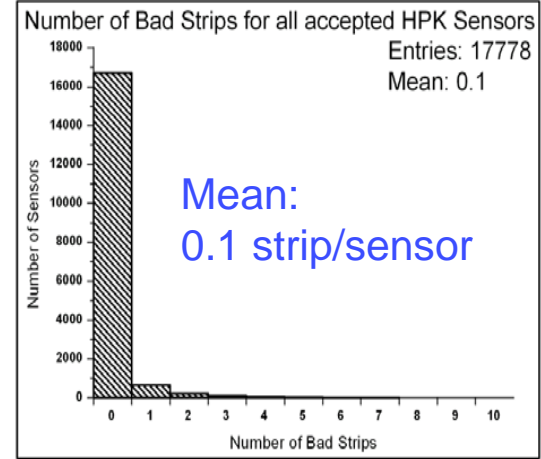
**Leakage Current at 450V**  
max: 10 $\mu$ A



**Full Depletion Voltage**  
Specification: 100 V – 300 V

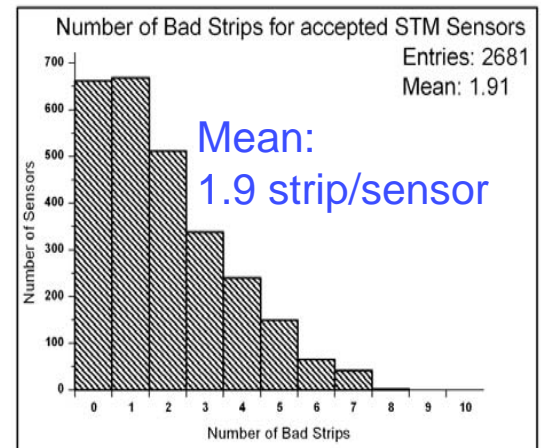
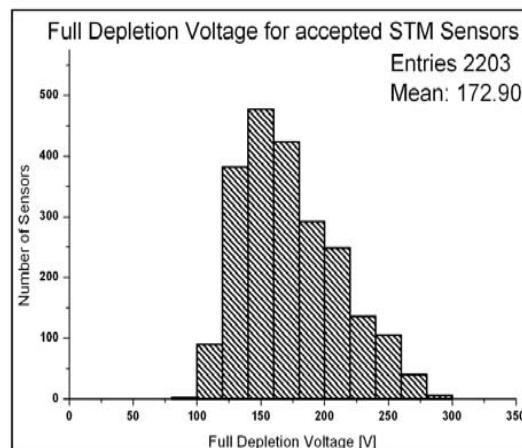
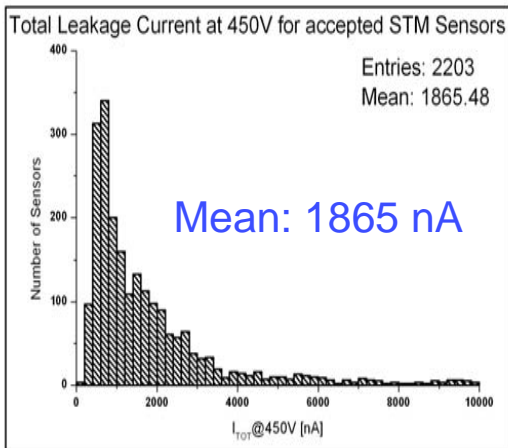


**Number of Bad Strips**  
max: 1% of 512/768 Channels



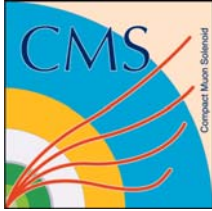
**Bad Strips**  
(pinholes,  
out of range  
poly-res,  
Al shorts,  
broken Al,  
open implants,  
leaky strips...)

0.02%



0.3%

**Overall percentage of good strips in accepted sensors: 99.94%**



# The front-end hybrid

- Diverse functionality - strict constraints: mechanical and electrical interface to pre-defined elements, low mass, high reliability, inexpensive

## 4-layer kapton substrate (flex) laminated to a ceramic carrier

- 12 different designs based on 3 layouts
- 120  $\mu\text{m}$  minimum feature size

## 4 or 6 APV25 readout chips

- Radiation hard 0.25  $\mu\text{m}$  CMOS technology (ASIC)
- 128 channels multiplexed to 1 analog output
- Charge sensitive amplifier with  $\tau=50$  ns, CR-RC shaper
- 192 cell pipeline (4.8  $\mu\text{s}$ ) per channel (trigger latency 3.2  $\mu\text{s}$ ) with max. 32 cell readout buffer
- Peak mode: 1 sample,  $\tau=50$  ns
- Deconvolution mode (for high luminosity): weighted sum of 3 samples,  $\tau=25$  ns, higher noise
- Multi mode
- Calibration circuit
- $V_{\text{supply}}$ : 0, 1.25, 2.5V
- Power / channel: 1.9 mW analog + 0.4 mW digital

## MUX (Multiplexer)

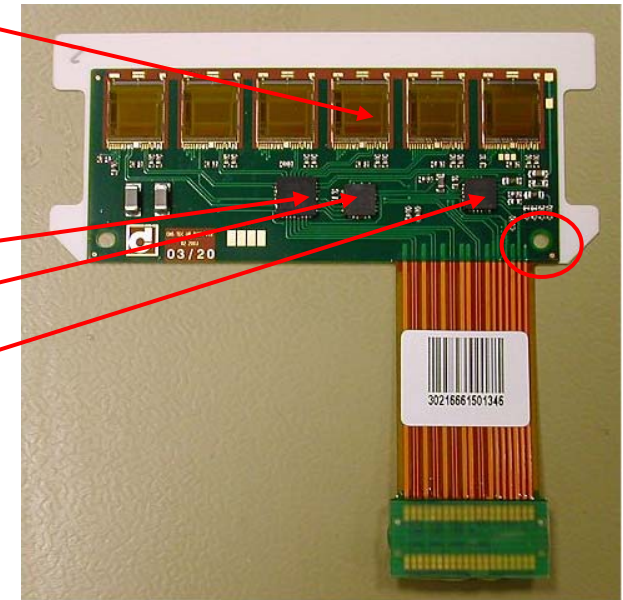
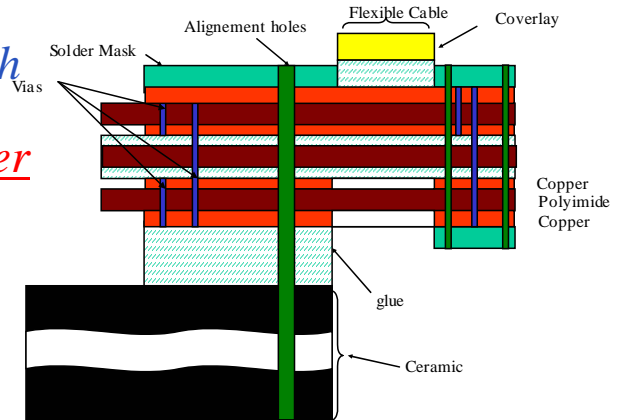
- 2 APV outputs multiplexed to 1 data line

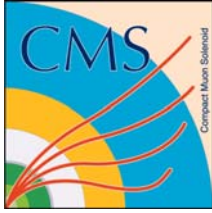
## PLL (Phase-locked loop)

- Decodes clock & trigger signals

## DCU (Detector Control Unit)

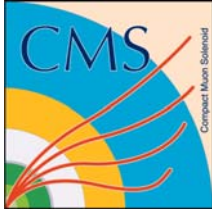
- 12-bit ADC for 8 channels: hybrid and sensor temperatures, leakage current, low voltages



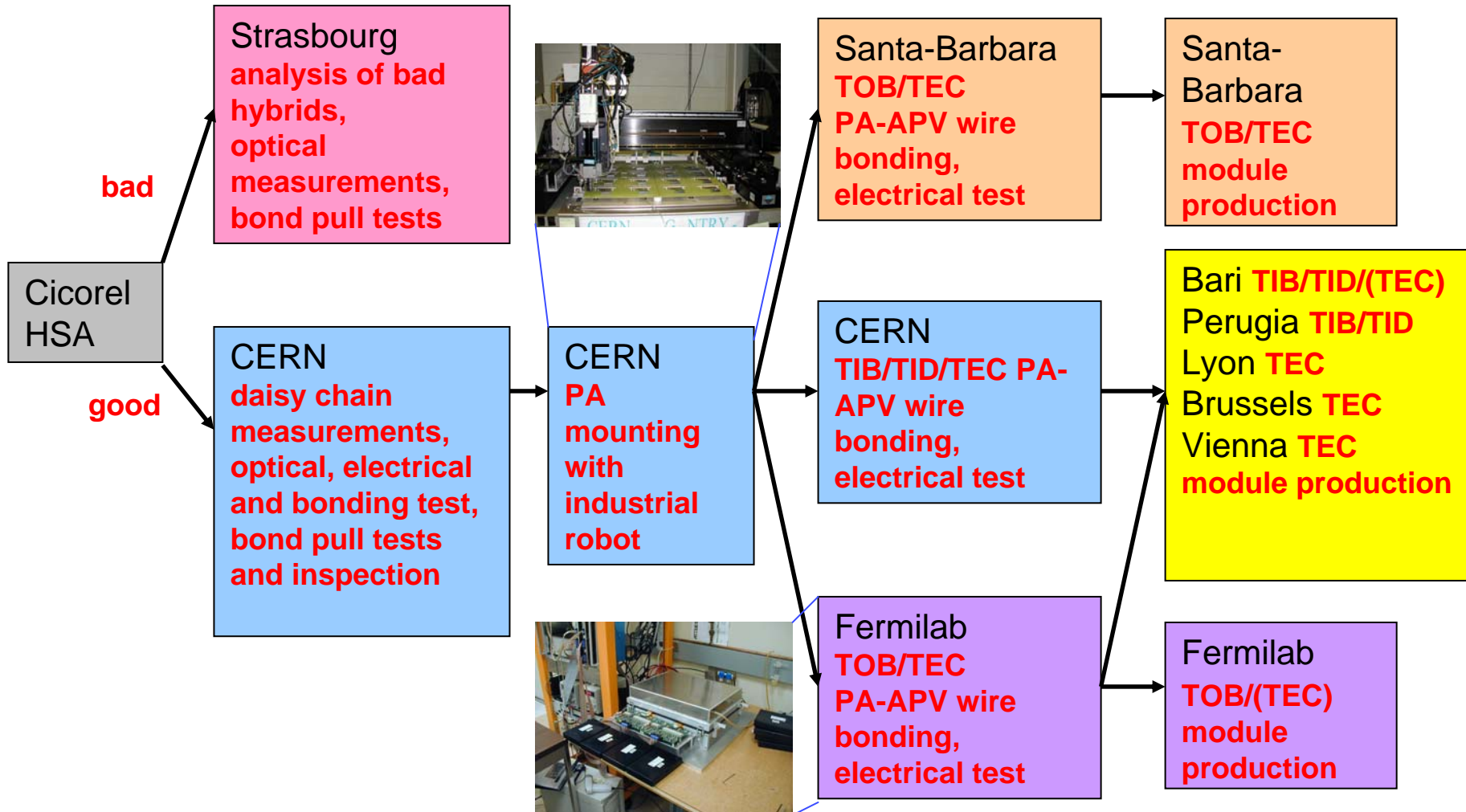


# *Front-end hybrid production*

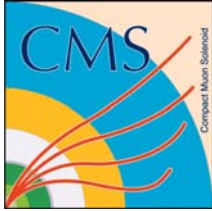
- *Flex circuits produced by Cicorel, assembly done by Hybrid SA*
- *Provided supplier with a test setup (Front-end Hybrid Industrial Tester)*
  - *Connectivity, electrical and functional tests in ~ 1 minute per hybrid*
- *Several problems at start-up and during mass production*
  - *Fast production (schedule pressure) but slow QA feedback (under staffed), problems typically found in production module tests*
  - *Producer does not analyse reasons for test failures*
  - *Irreversible addition of value when mounting hybrid on sensor module, large effort to recover*
- *Response to problems*
  - *Strengthen QA/QC at producer and within CMS*
  - *A CMS collaborator visit the producer weekly to check production quality (analyse rejects)*
  - *Establish a backup hybrid line (flex circuits from GS Präzision, assembly at AmTech)*
- *Production rate finally stabilized at 300 then 400 hybrids / week*



# Front-end hybrid flow

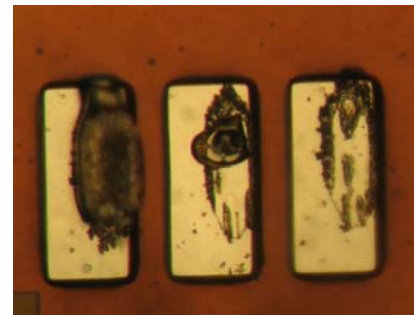
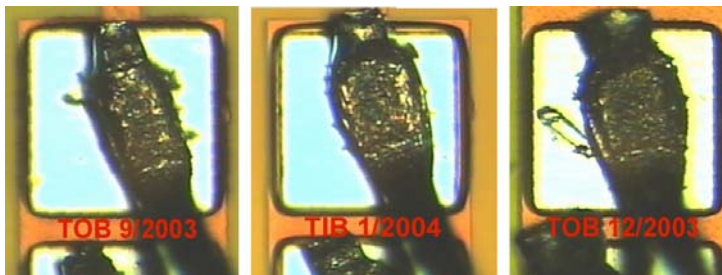
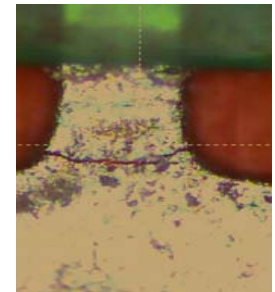




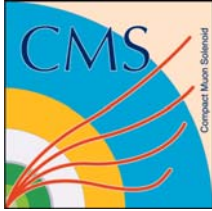


# Problems during production

- *Lamination on ceramic (at start up)*
- *Cracked lines near connector – fragile Ni/Au pads on kapton  
solution: FR4 rigidifier + layout modification  
900 hybrids and 2 months lost*
- *Contamination of connectors by solder flux and other residues from the production process of adapter cards (cleanable but complicates life and if not discovered before mounting modules on structure may cause intermittent failures during operation)*
- *Batches with unreliable wire bonds due to overdeformation  
840 hybrids and 270 modules affected*
- *Bonding quality and long-term reliability remains main concern (over-deformation, cratering)*
  - *Large (continuous) effort to optimize and maintain bonding quality with several production stops*



**Cratering:** damage to Si during bonding  
- very nasty problem, raising long-term reliability issues but...  
- our excessive aging tests did not reveal problems (we had to accept these hybrids)

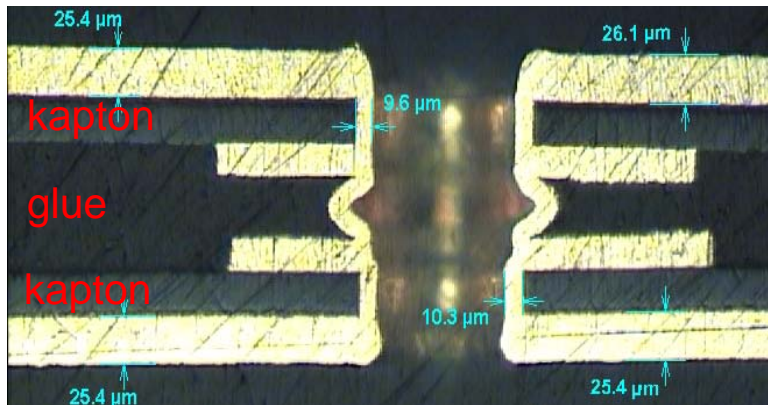


# Problems during production

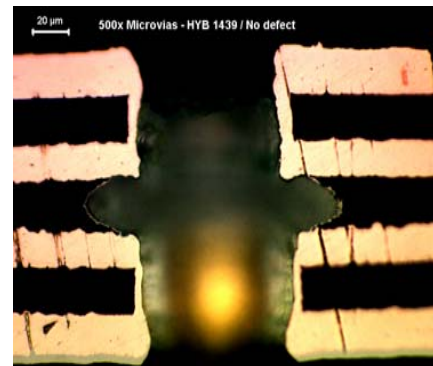
## Unreliable via's

- *100  $\mu\text{m}$  vias with broken contact found in module long-term test (anomalous pedestal behavior in a 10-20 minute period)*
- *hidden defect, not picked up by fast FHIT test ( $\sim 1$  minute)*
- *all vias tested at Cicorel, these must have broke during further processing*
- *serious concern with hybrid reliability:  
 $\sim 2180$  hybrids rejected and  $\sim 320$  modules affected*
- *solution: increase via diameter to 120  $\mu\text{m}$  and add additional kapton layer*

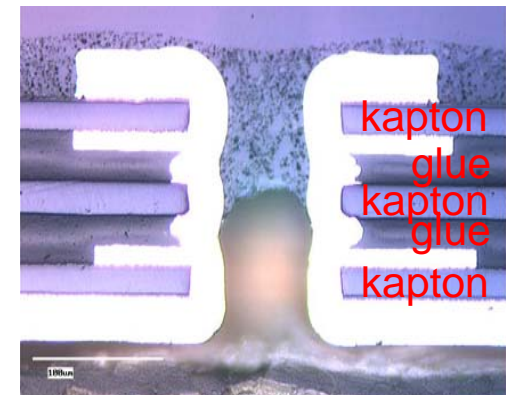
Old design

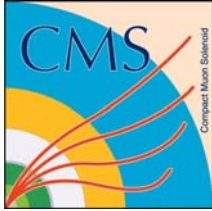


Bad via



New design

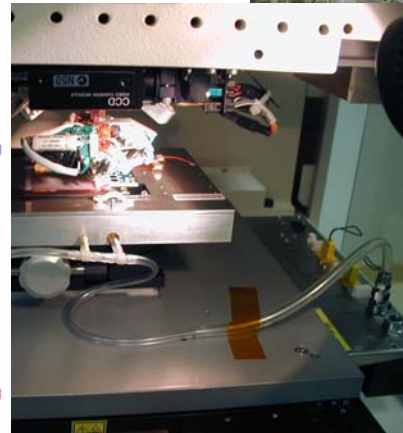
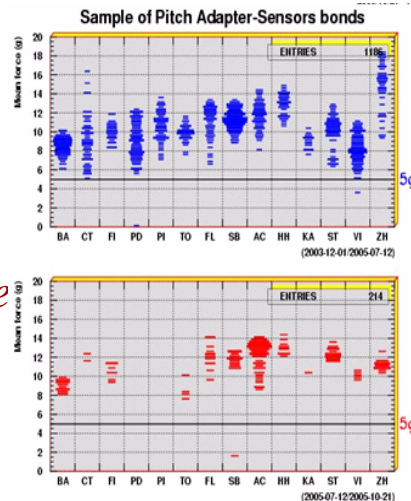
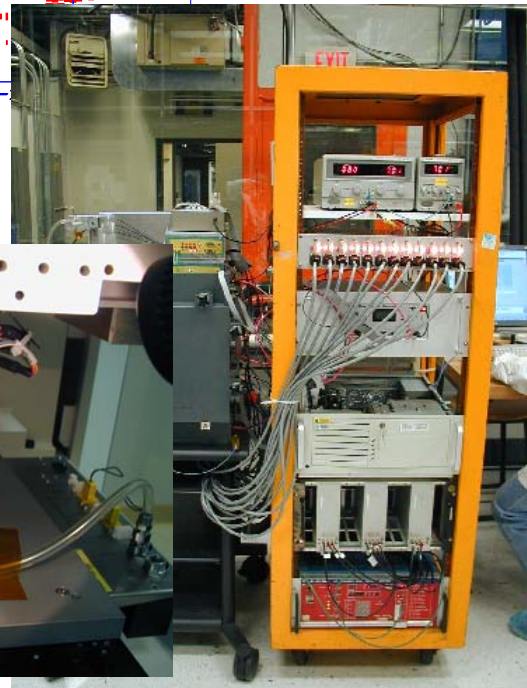
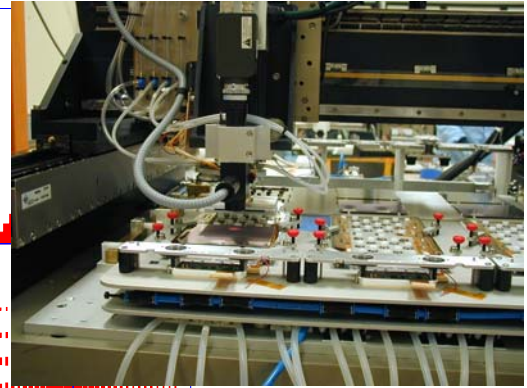
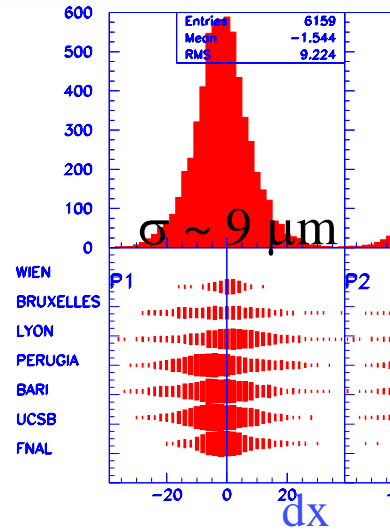


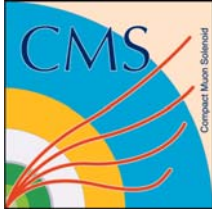


# Module production and test

XMeas. – XNom. Sili1

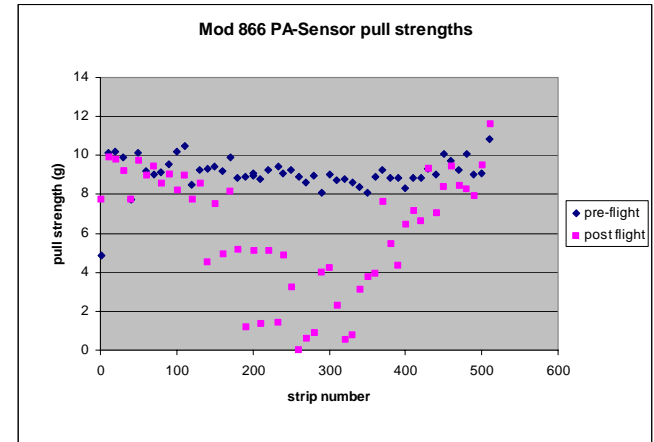
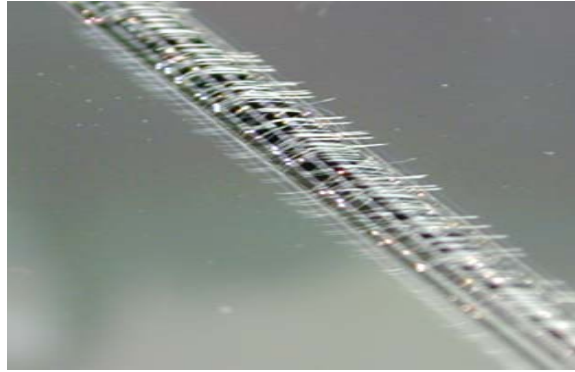
- Robotic assembly of modules at 6 gantry centers: gluing the sensors and the FE hybrid to the frame with high precision (e.g.  $x \perp$  strips  $< 39 \mu\text{m}$ )  $\rightarrow$  99% within specifications
- Wire bonding between pitch adapter and sensor(s) using 23 automatic bonding machines
- Single module test with ARC (APV Readout Controller) and long-term test using a CMS like DAQ system including several thermal cycles:
  - Noise
  - Pulse-shape
  - Bad strips (disconnected/shorted strips, pinholes, noisy channels)
  - IV curve
  - Pipeline errors
- 0.1-0.3% of bad strips/module
- Module production yield (including mechanical and electrical problems): 94-99%





# Module production problems

☞ *Damage to wire bonds during shipping of modules from US to CERN (due to vibration)*  
→ *Needed to modify and then verify module design*



*TOB solution: reinforce the module by adding Si based Sylgard glue (elastic from  $-50 \rightarrow 200^{\circ}\text{C}$ ) line to the back side*

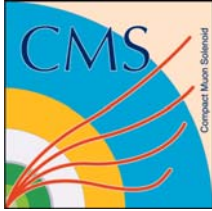
*TEC solution: add a ceramic stiffener under the bonding area*

*Tests performed:*

- *Drop and vibration tests*
- *FEA analysis,*
- *Thermal efficiency (cooling) measurements on module and on substructure*
- *Deformation study (due to bimetal effect)*

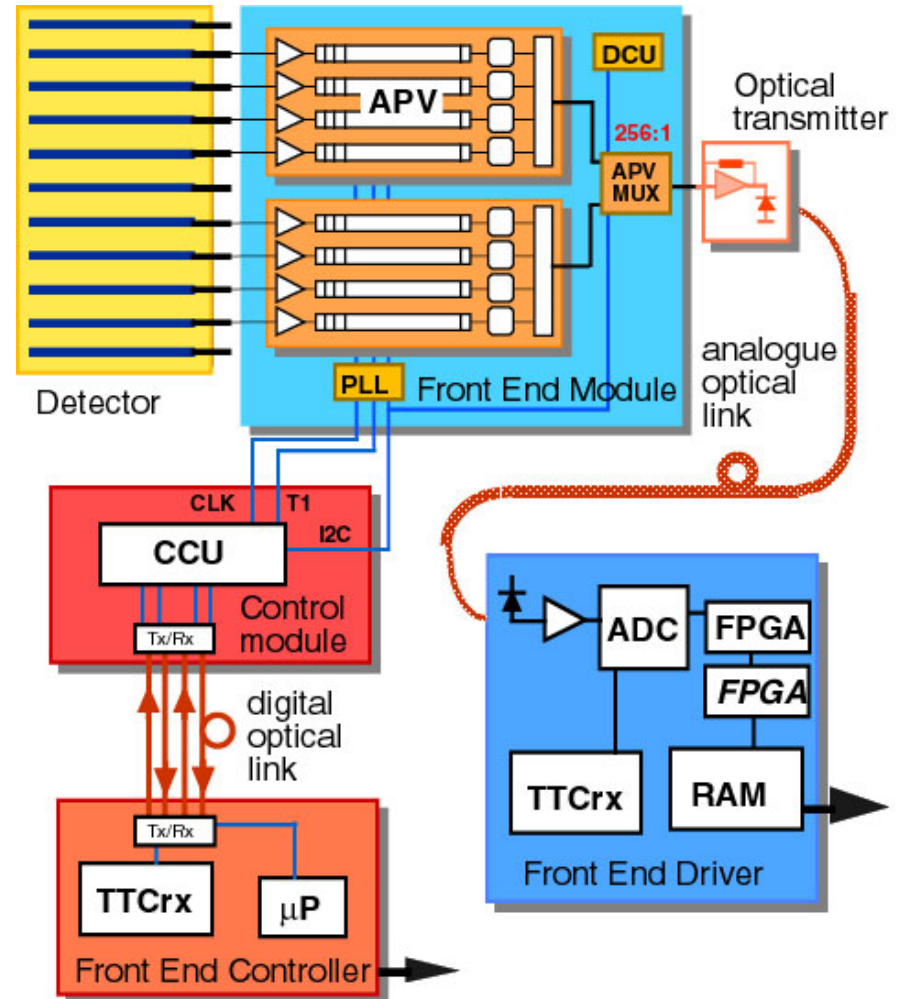
☞ *Unreliable bias connections (failing conductive epoxy)*

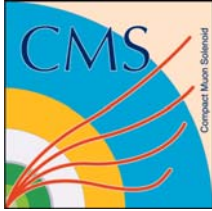
→ *Need to re-work (wire-bonding + electrical test) ~1400 TOB modules*



# Readout and control architecture

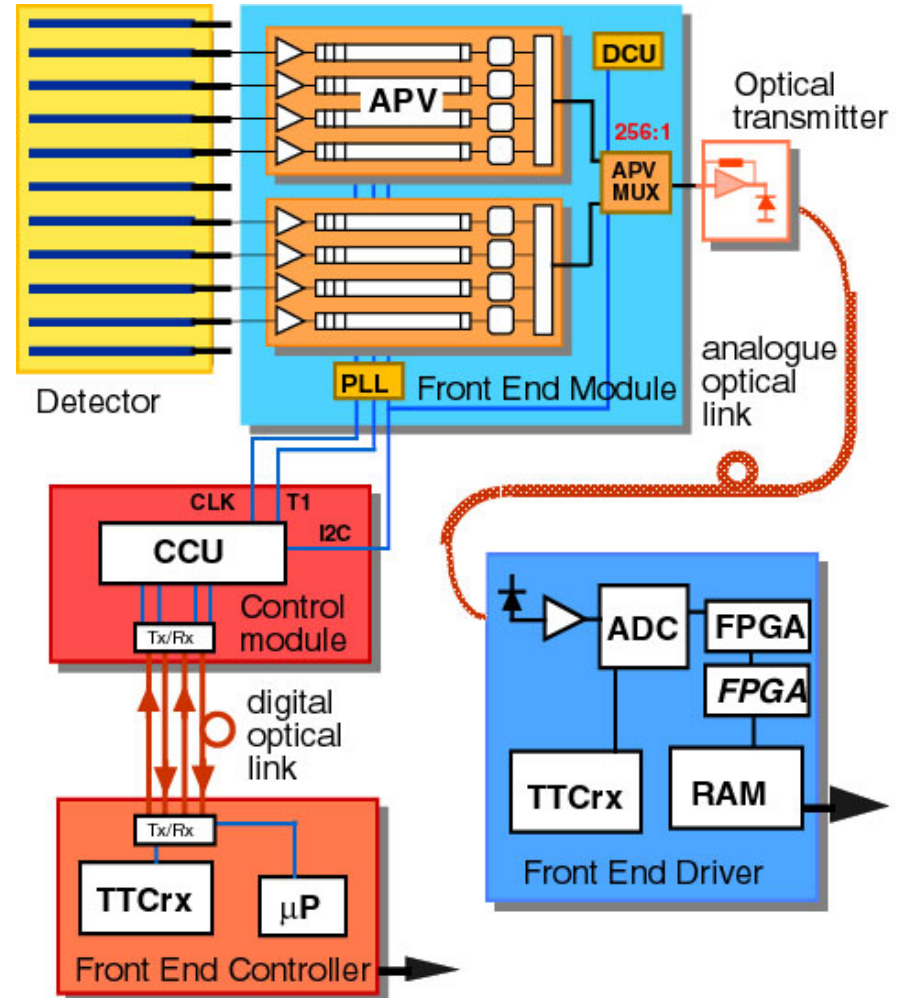
- APV25 readout chip samples and then stores the analog pulse height data for first level trigger latency of  $3.2 \mu\text{s}$
- On L1 trigger, the pulse height data processed and multiplexed from pairs of APVs onto a differential line to the Analog Opto-Hybrids (AOHs) located at a few cm distance
- AOHs consist of laser drivers that modulate the drive currents of edge-emitting InGaAsP laser diodes ( $\lambda=1300 \text{ nm}$ )
- After electrical to optical conversion the data is transmitted over  $\sim 100 \text{ m}$  single-mode fiber optic cable to the Front-end Drivers (FEDs) in the counting room ( $\sim 40000$  links @  $40 \text{ MS/s}$ ,  $\sim 50 \text{ mW}/256$  channels)
- The FEDs, 96-channel VMEbus readout boards equipped with optical receivers, provide digitization (10-bit ADC) and first processing of the data (frame finding, reordering, pedestal subtraction, optionally cluster finding)
- The data is stored in a local memory until requested by the higher levels of DAQ
- FED input data rate:  $3.4 \text{ GB/s}$ , output rate:  $200 \text{ MB/s}$



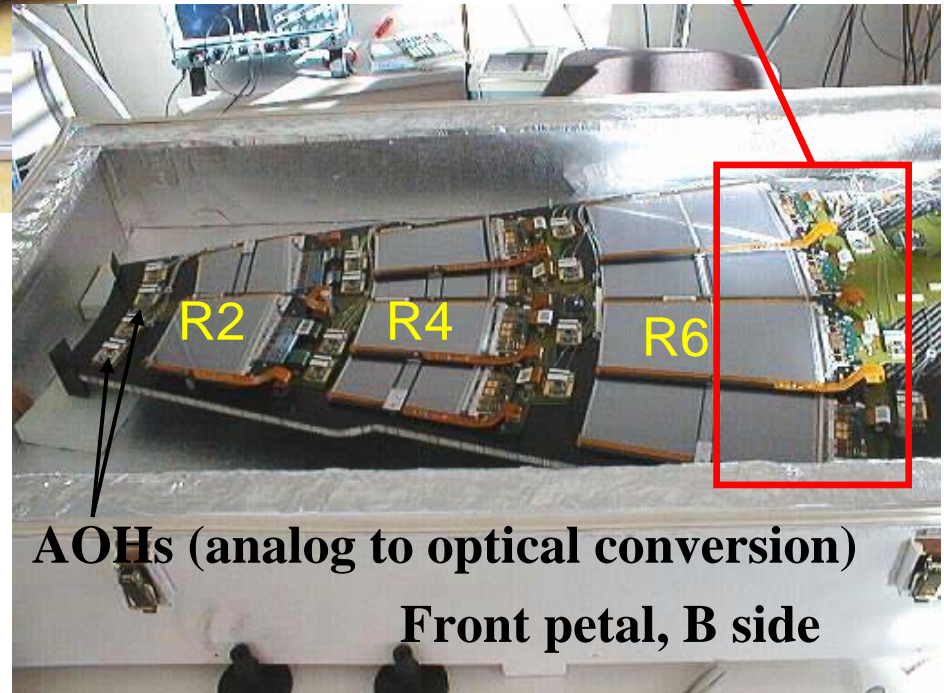
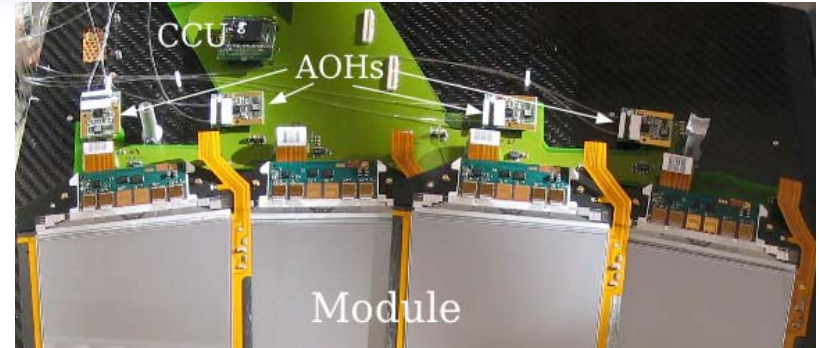
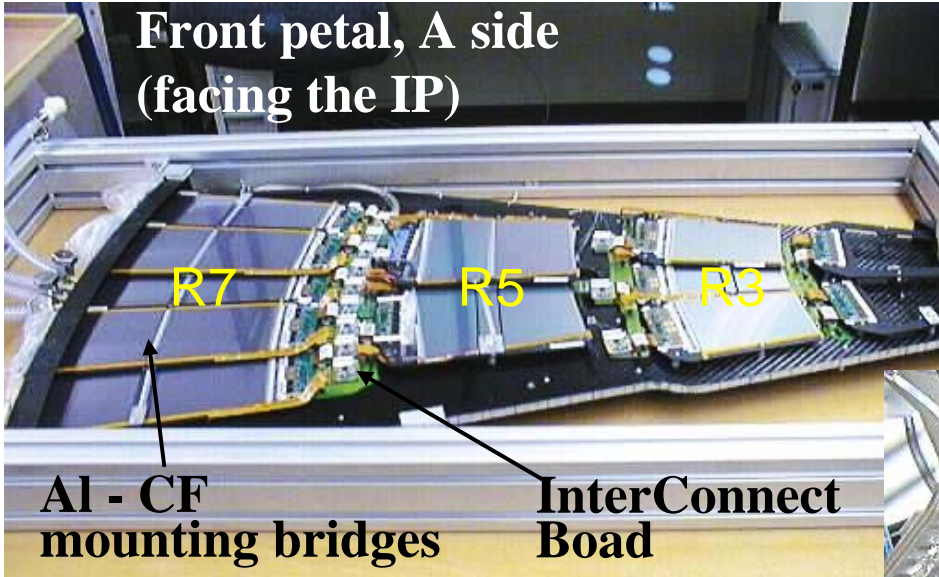


# *Readout and control architecture*

- The control of electronics is handled by VMEbus boards, the Front-end Controllers (FECs)
- FECs distribute the LHC clock (40.08 MHz) and L1 trigger signals (up to 100 kHz rate) received from the global LHC Timing and Trigger Command (TTC) system
- Timing and control signals are transmitted to the detector via digital optical links (~2000 links @ 40 MHz)
- The signals are encoded by the Communication and Control Units (CCU25 chips) mounted on small PCBs called CCU modules (CCUM) located on the motherboards of the petal, rod,...substructures
- CCU25 communicates with FEC via token ring protocol and distributes timing signals to the PLL chips located on the FE hybrids
- Control signals are distributed via I<sup>2</sup>C protocol to the addressed chips on the appropriate devices (modules, AOHs)

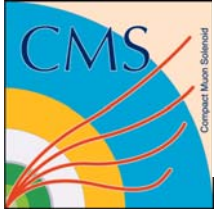


# TEC sub-structure: petal



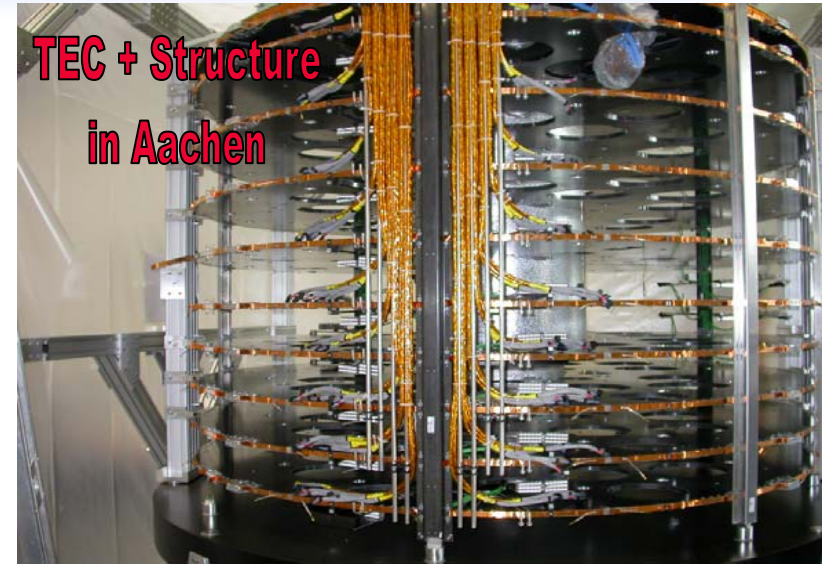
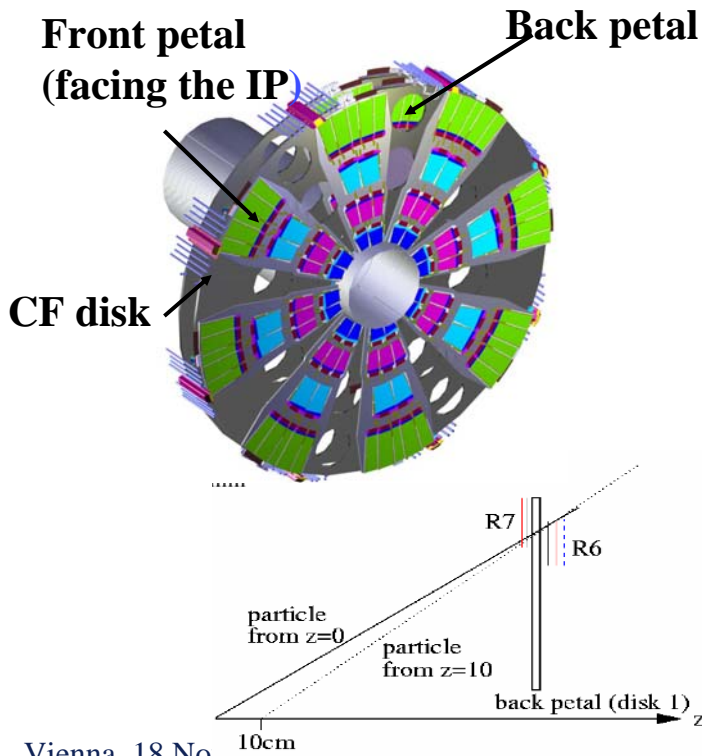
## Cooling:

- titanium pipe of 3.9 mm inner diameter and ~7 m length embedded inside the petal
- modules and AOHs mounted on Al inserts glued directly to cooling pipe (precision mounting, thermal contact)



# TEC structure

- 2x9 CF disks
- basic substructure - petal: wedge shaped CF support plate carrying up to 28 wedge shaped modules arranged in 7 radial rings
- Front and back petals have different geometry and have different number of modules



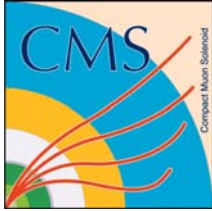
*Clearance problem spotted during petal assembly (after the completion of petals for sector 1). Why not before?*

- Height of individual components on Inter-Connect Board not implemented on mechanical drawings
- Tolerances (petal thickness, module leg thickness, height and soldering of components...) are large and poorly known
- Mechanical forces are apparently small and no electrical problem occurred

*Implications of design change*

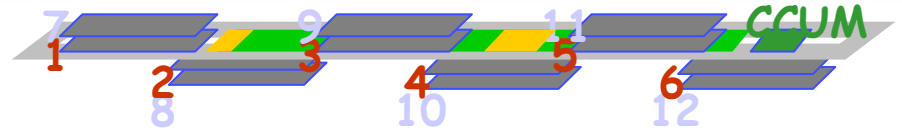
- Cooling performance still seems ok but worse by 1.58 K/W
- Hermeticity for particles still sufficient

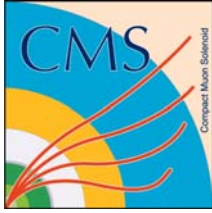




# TOB sub-structure: rod

- 6 cylindrical layers
- basic substructure - rod:  
carbon fiber support frame carrying 3  
single or 3 double sided thick modules on  
each side



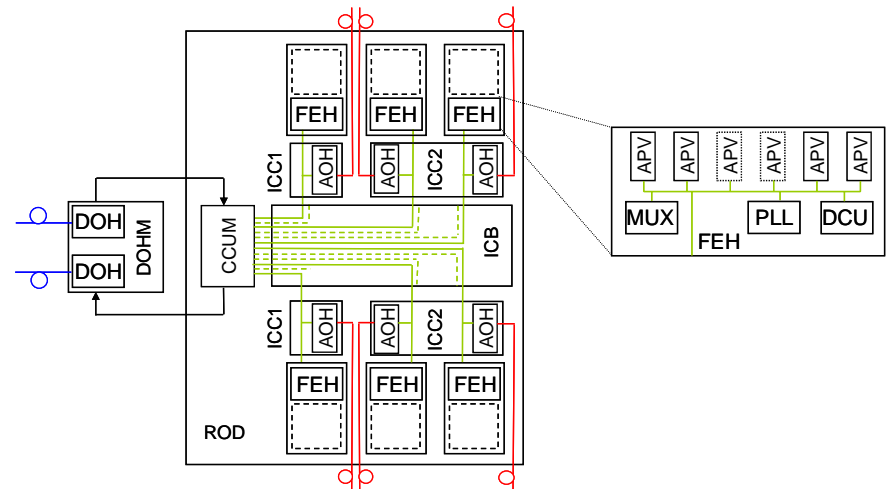
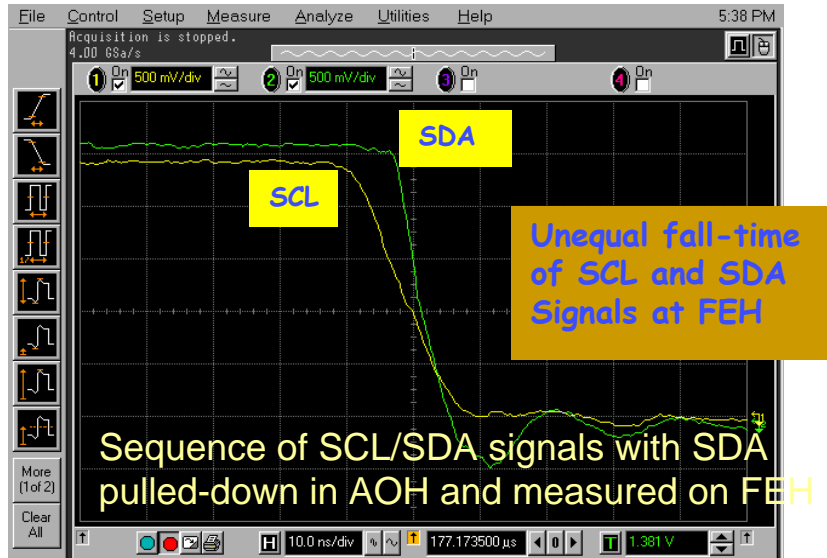


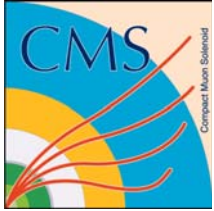
# I<sup>2</sup>C problems on TOB rod

I<sup>2</sup>C errors were seen in first TOB rod acceptance tests at low T

- Combination of several factors:
  - Series resistors plus parasitic capacitances → different timing behaviors of APV on FEH and LLD on AOH
  - Different logic thresholds on APV and LLD
- For certain data patterns, AOH acknowledge interpreted as I<sup>2</sup>C 'start' by APV giving an I<sup>2</sup>C error
- From several solutions considered the final choice: redesign Inter-Connect Cards (achives ~10 ns margin in SCL and SDA edges on FEH)
- Important to have good margin as timing affected by temperature, radiation damage (and ageing?)

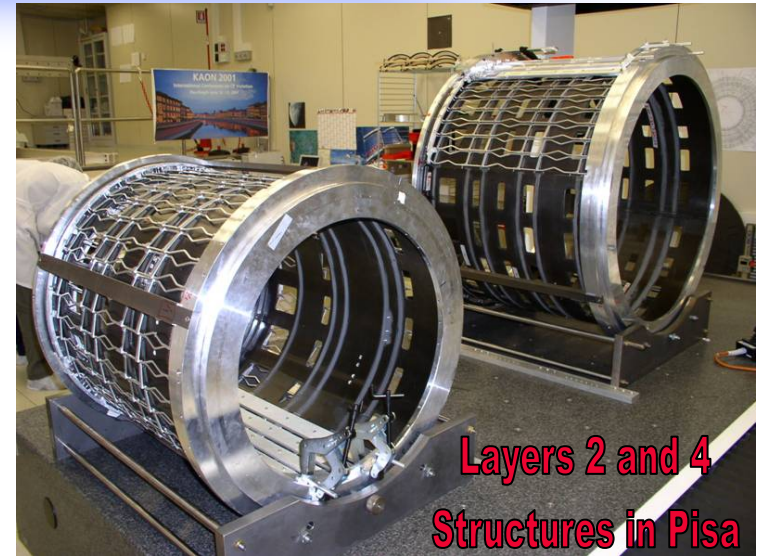
TEC: safety margin is 8 ns, with the addition of a single capacitor in the SDA line extra 3 ns gained  
 TIB: seeing I<sup>2</sup>C errors in layer burn-in (may not be the end of the story...)

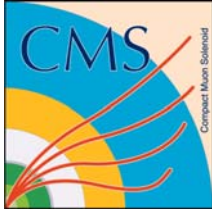




# TIB structure

- 4 cylindrical layers each made of 4 carbon-fiber half-shells
- strings of 3 thin modules are mounted inside and outside the half-shells





# TIB layer 3 burn-in

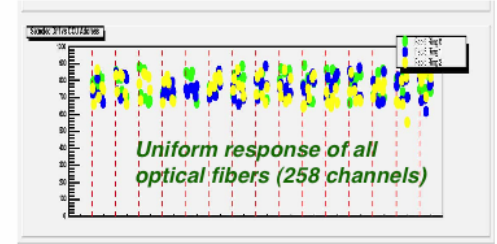
## Burn-in procedure

- Connect optical fibers to optical ribbons
- Connect power cables and switch on
- Timing alignment of all modules
- Optical gain adjustment
- Take data (peak and deconvolution mode)
- Flux dry air & cool down climatic chamber and repeat data taking
- If some modules do not respond to I2C communication: change the modules (~2%)



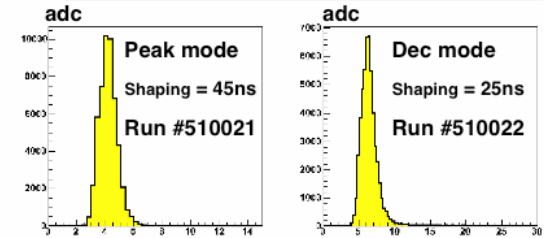
## Analog Opto Hybrid gain:

- Gain is adjusted according to the tick mark



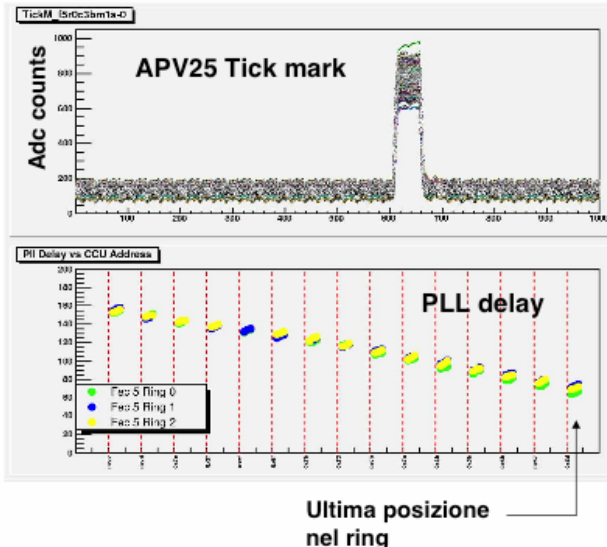
## Data taking:

- Parallel readout of the full layer
- Noise distribution in peak mode and deconvolution mode (66048 strips)



## Timing:

- All modules (129) are powered
- All tick marks are reconstructed and aligned
- PLL delays are selected to synchronize all modules: *consistent with logical position inside the ring*

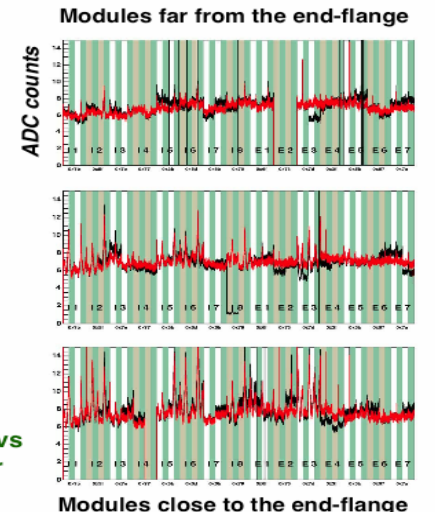


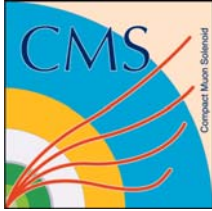
Peak mode: very good noise distribution

Dec mode: higher noise in modules close to the end flange

-> better grounding & shielding needed

Noise Distribution vs Strip number (DEC INV-ON)





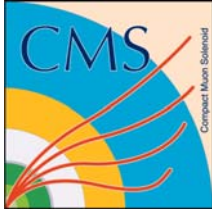
# *TID structure*

- *On 2 x 3 disks modules arranged in 3 rings*
- *Delay due to late delivery of DAQ SW + time spent on fighting noise*



QuickTime™ and a  
TIFF (Uncompressed) decompressor  
are needed to see this picture.

*2 control rings:  
Noise tails on ring 2 become more  
pronounced when ring 1 is on  
Final grounding and shielding scheme  
to be implemented*

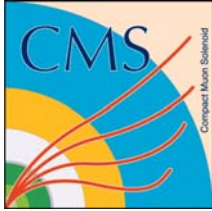


# Integration test



July 2005





# Substructure performance in testbeam

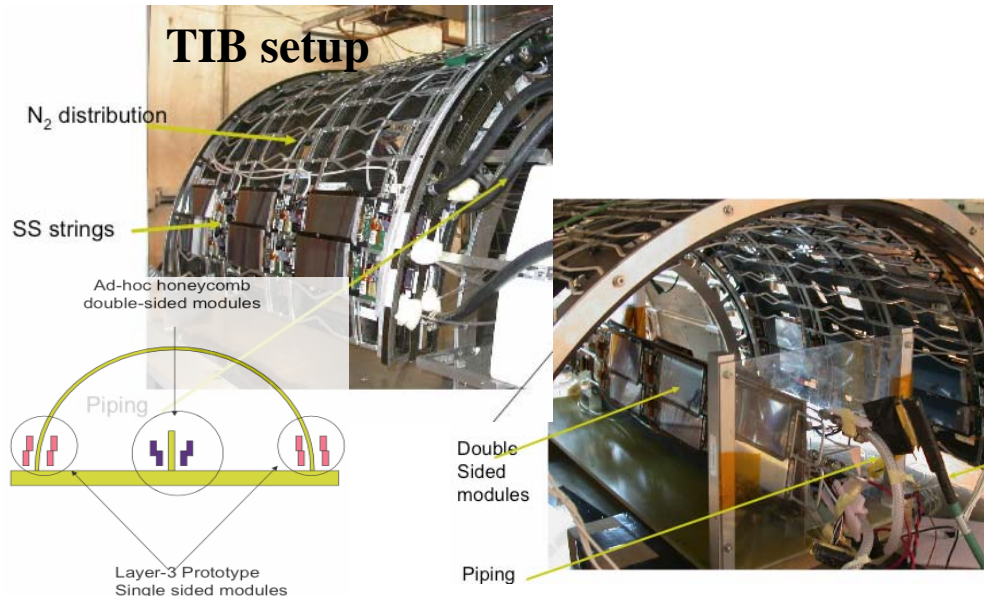
Beam: 120 GeV pions or 70-120 GeV muons at CERN X5 in 2004

Including:

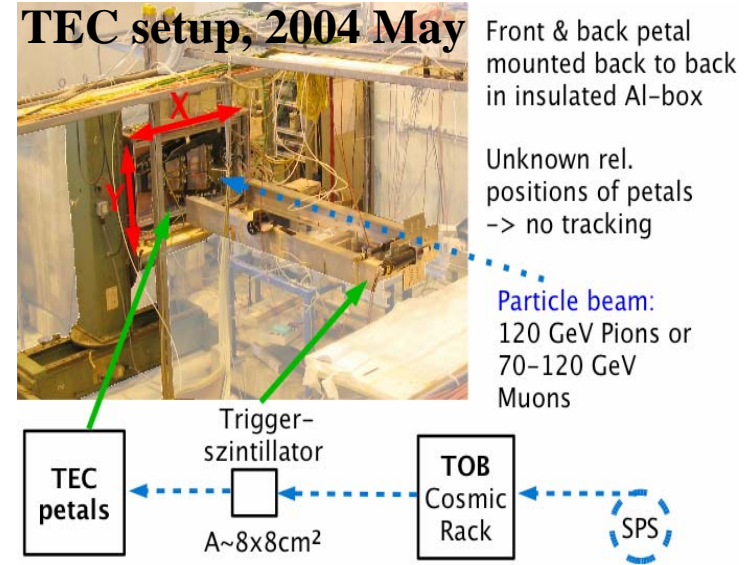
- 2 TEC petals (1 complete control ring with 51 modules, 1% of TEC)
- 6 TOB rods in Cosmic Rack (precision support structure with integrated cooling) operated at CMS temperature (~-10 C)
- TIB Layer 3 prototype plus some double sided strings on an ad-hoc structure

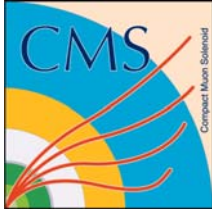
Excellent system behavior:

- Stable communication and readout at all temperatures
- Uniform noise distribution, small common mode
- Signal/noise ~ 20
- Equivalent noise charge consistent with expectation from measurements on single APVs

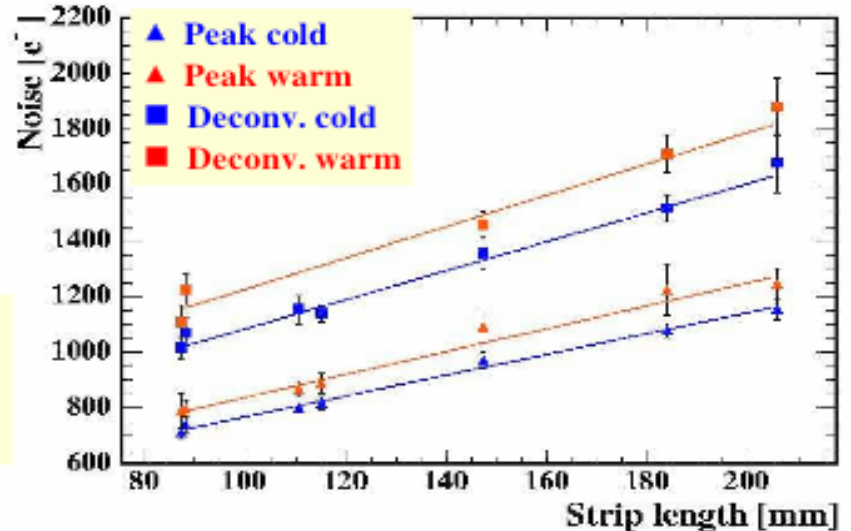
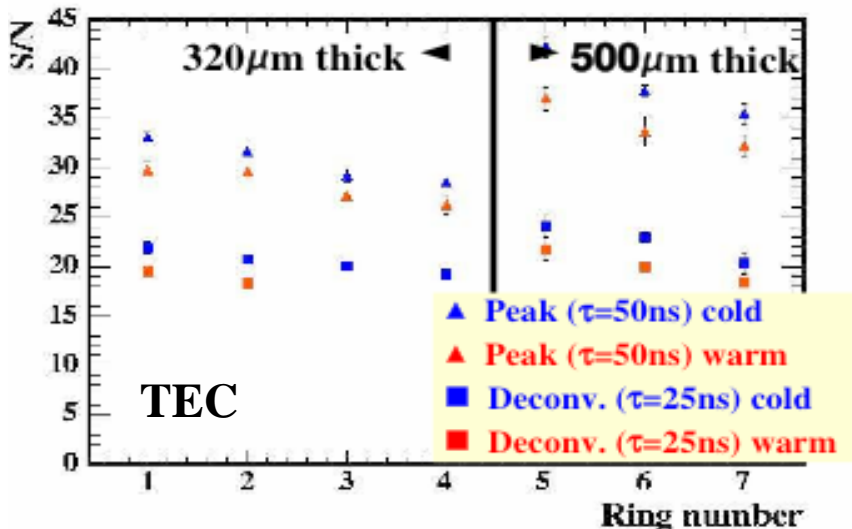
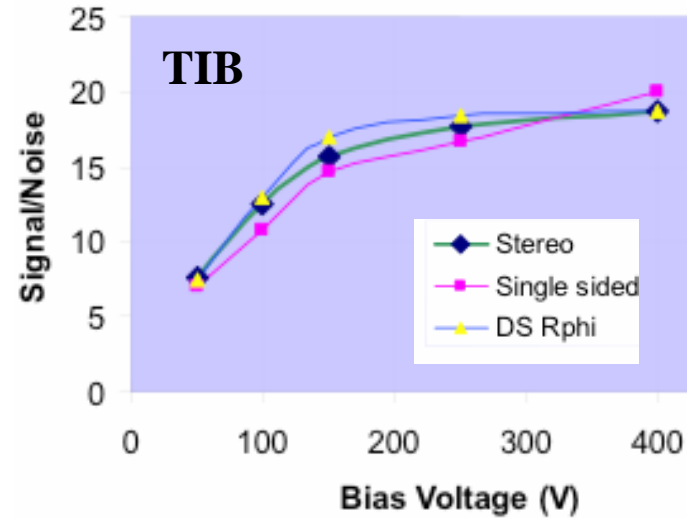
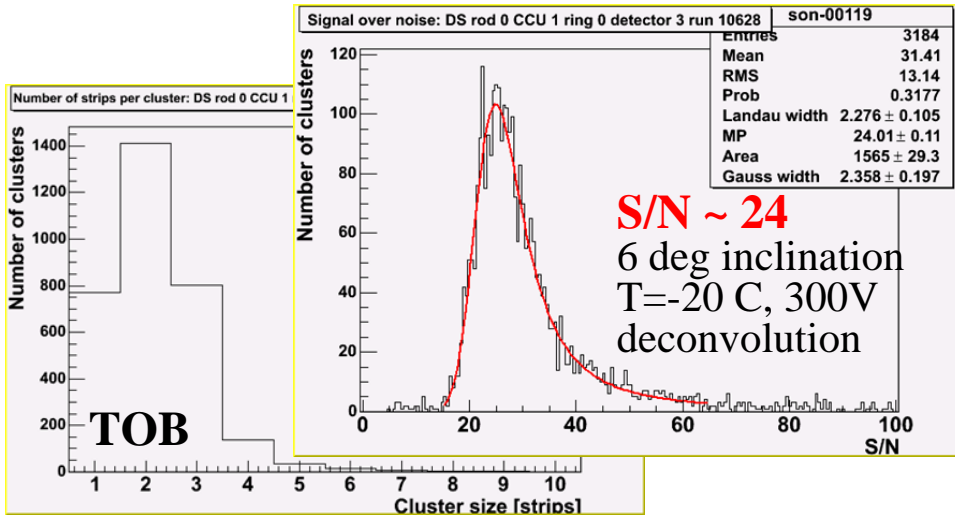


## TEC setup, 2004 May

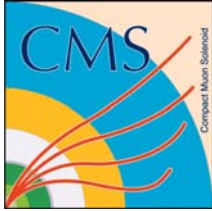




# Signal / Noise performance



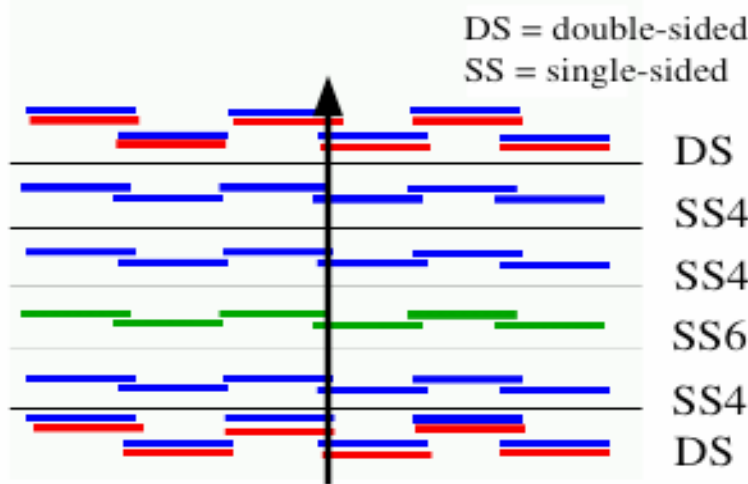
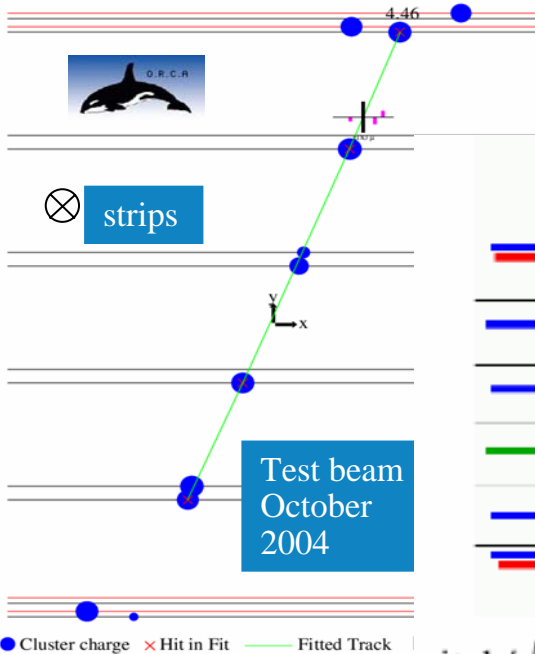
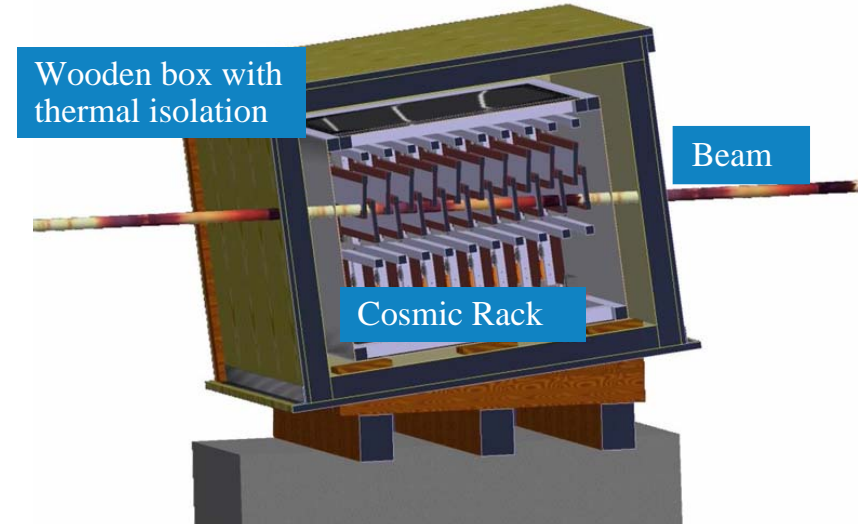




# Tracking studies with Cosmic Rack

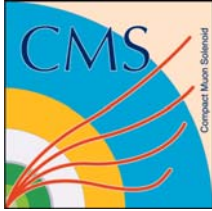
Successful tracking in Cosmic Rack using official CMS software (ORCA)

- Spatial resolution agree with expectation
- Resolution improved for inclined tracks as in average 2 strips are hit



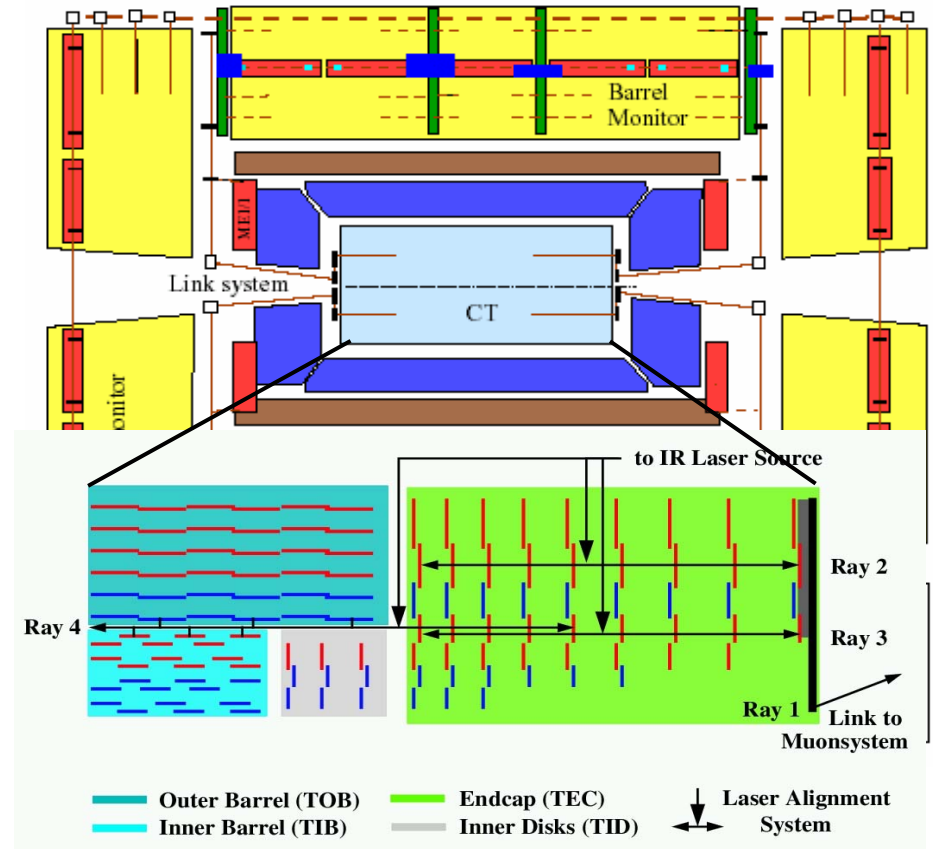
	$\sigma_{\text{may}} (\mu\text{m})$	$\sigma_{\text{sept}} (\mu\text{m})$
	straight tracks	6° inclination
DS	45	29
SS4	45	34
SS4	42	38
SS6	35	28
SS4	54	33
DS	50	30

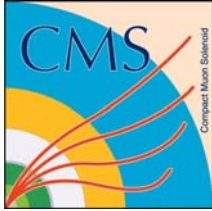
pitch/ $\sqrt{12}$ =35 $\mu\text{m}$  (SS6) and 53 $\mu\text{m}$  (DS, SS4)



# Alignment system

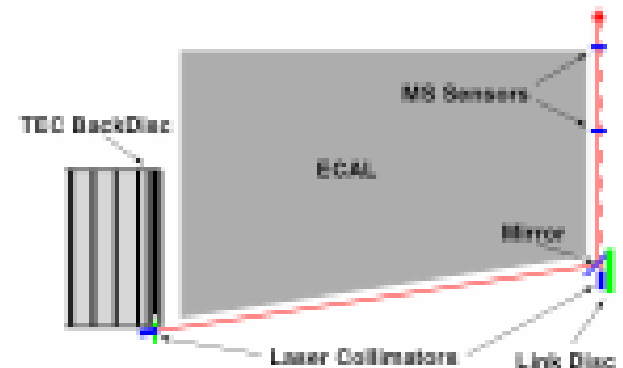
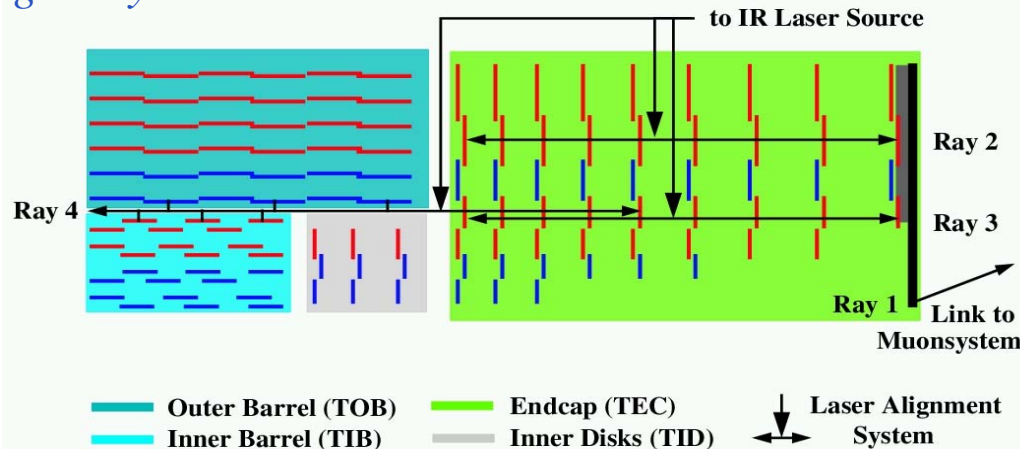
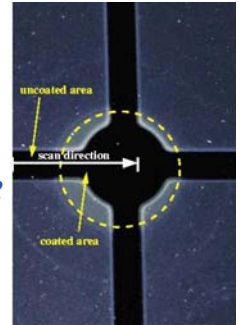
- *Assembly tolerances and measurements:*
  - *individual silicon modules within SST subdetectors  $\sim 50 \mu\text{m}$*
  - *between subdetectors  $\sim a \text{ mm}$*
- *Intrinsic detector resolution  $10 (r-\phi)$  and  $17 (r-z) \mu\text{m}$  for pixel and  $\sim 10-60 (r-\phi)$ ,  $500 (r-z) \mu\text{m}$  for an SST module*
- *Using charged tracks and minimizing the track residuals in an iterative procedure, the tracker can be aligned to  $\sim a \text{ few } \mu\text{m}$*
- *... but tracking must be possible for the iteration to be started  $\rightarrow$  hardware alignment system (MB, ME, SST, Muon - SST link system):*
  - *Monitors the tracker support structure (mechanical and thermal stresses may change the alignment during operation), generates online alignment info*
  - *Provides an absolute detector positional accuracy of  $100 \mu\text{m}$ , needed for tracking input to HLT*

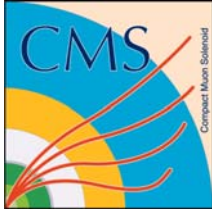




# Implementation

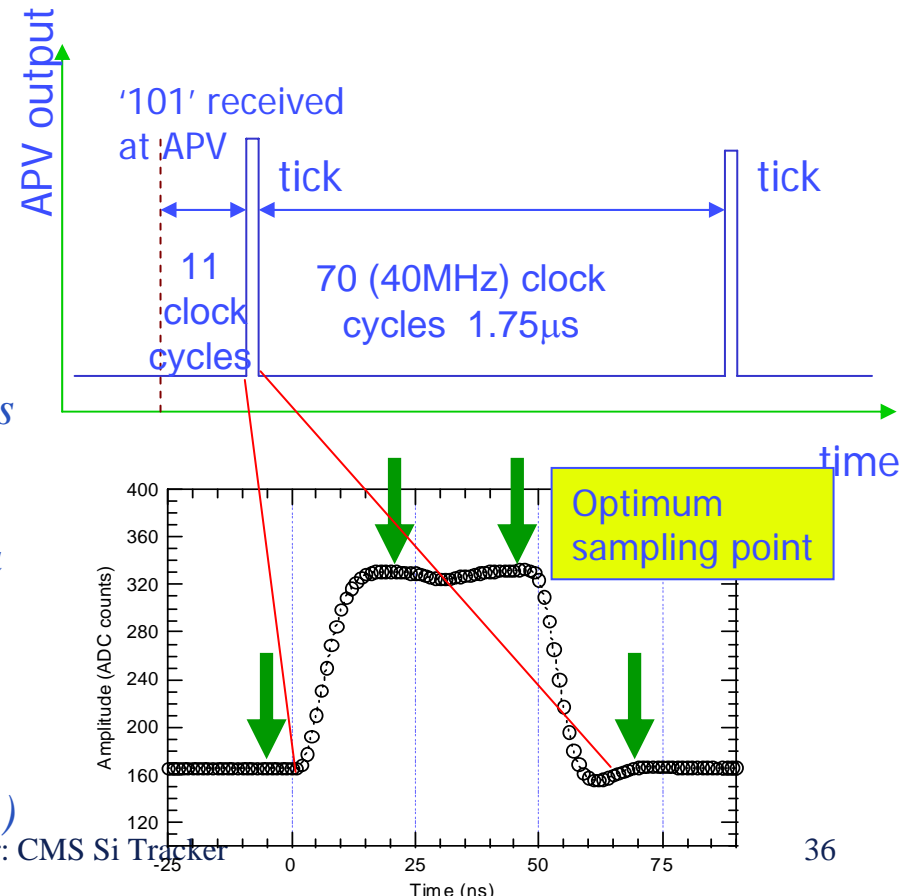
- *Si partially transparent to IR light*
- *Hit Si with (naturally straight) laser beams of  $\lambda=1080$  nm that are sensed by the modules*
  - *Ray 1: 6 beams couple SST to muon system on each side of the SST*
  - *Rays 2, 3: 2x8 beams (symmetrically distributed in  $\phi$ ) align all 9 TEC disks*
  - *Ray 4: 8 beams connect TIB, TOB and and both TECs*
- *For each beam, the laser coupled to an optical fiber and fed into a beam splitter*
- *Use special sensors for endcap with anti-reflective coating and a 10 mm hole in the back-metal which enables the transmission ( $\sim 20\%$ ) of the laser beams*
- *Tilt-meters also monitor the orientation of SST and the muon system wrt to gravity*



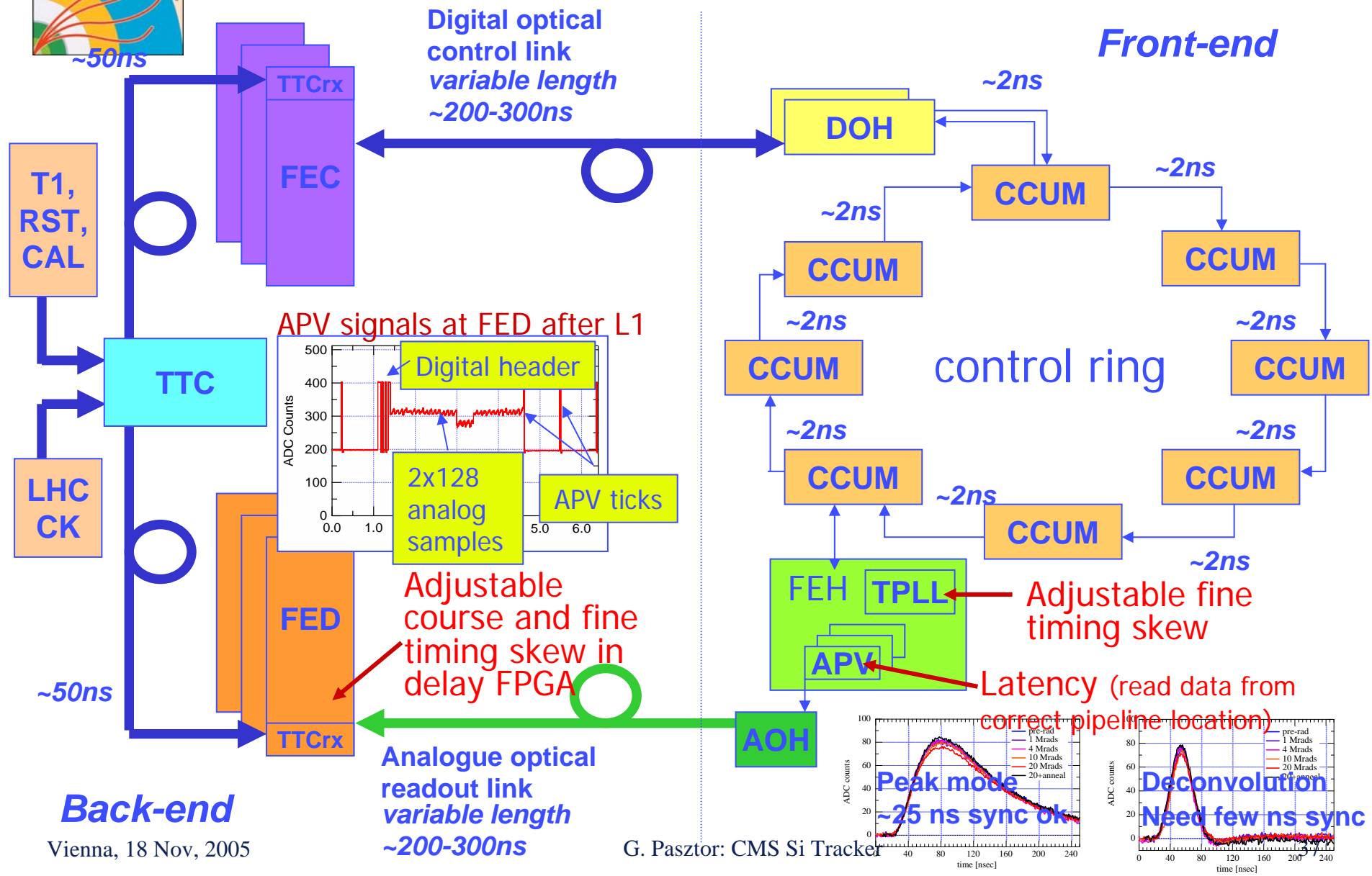


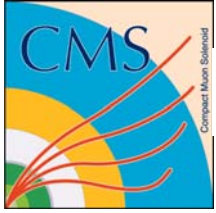
# Synchronization

- Readout system as a whole need to be synchronized in order to capture data from the correct bunch crossing (25 ns) and to transmit this data with optimal signal-to-noise ratio, i.e. sample the analog data at the appropriate moment in time (with few ns tolerance)
- Achieved in two steps: (1) relative: APVs with each other (2) absolute: SST to LHC beam
- **Main tool: APV 'ticks'** every 70 clock cycles starting at a fixed time after 're-synch' (101) signal received at APV, providing precise probes of timing skew between APVs
- Tick transmitted over analogue optical link to FED
  - Measure arrival time at FED
  - Calculate time when the ticks left the APVs knowing analogue optical link lengths from database
- **Intelligence: FED** reads 8•12 inputs and has a delay FPGA before front-end FPGA
  - Coarse clock skew (25ns steps) to analyse same APV sample across the 12 inputs
  - Fine clock skew (1ns steps) to allow enough settling time for the signal (~20ns)

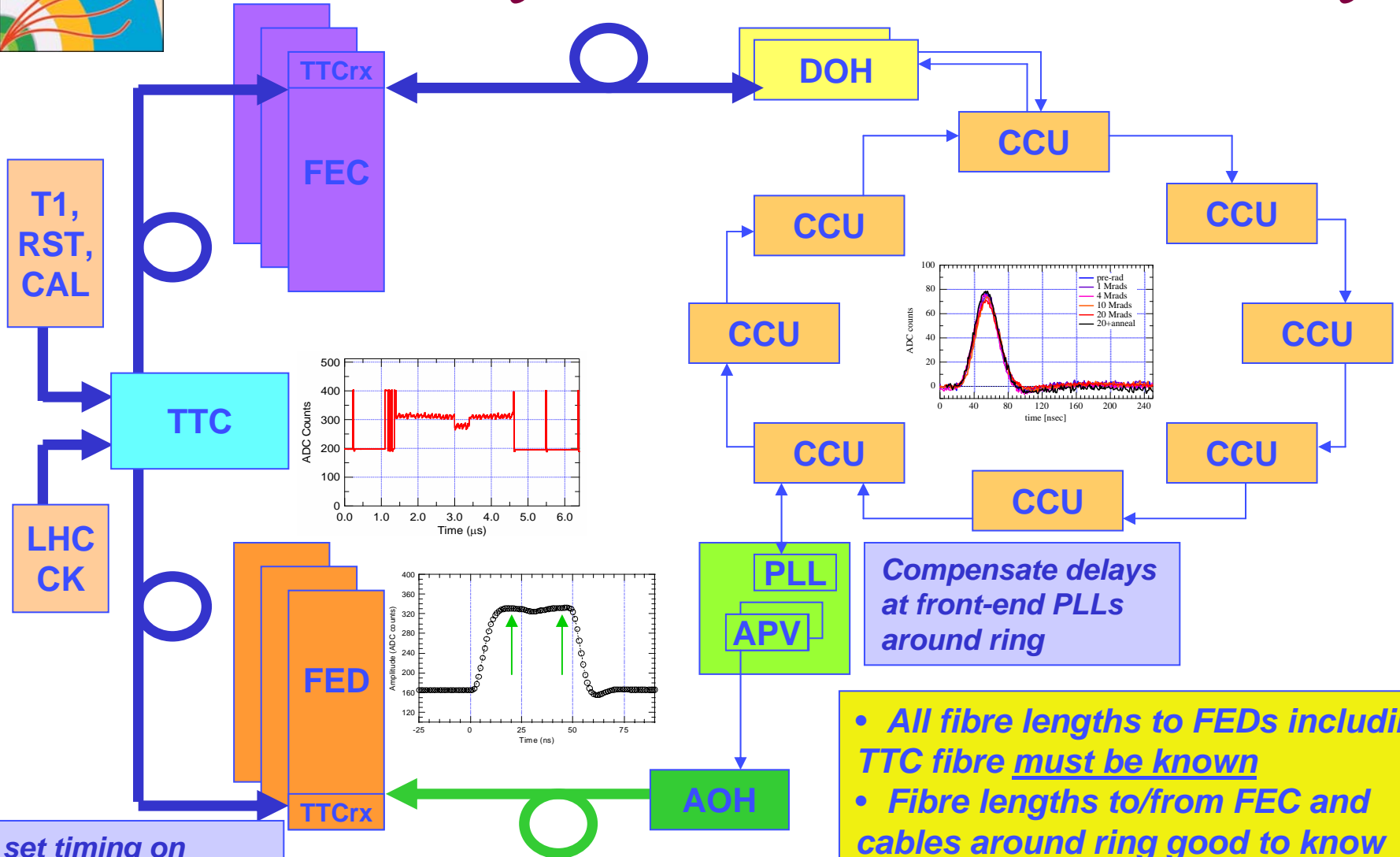


# Timing issues in readout system





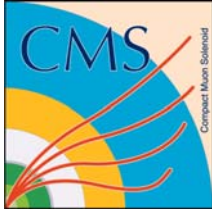
# Relative synchronization summary



set timing on delay FPGA at FED

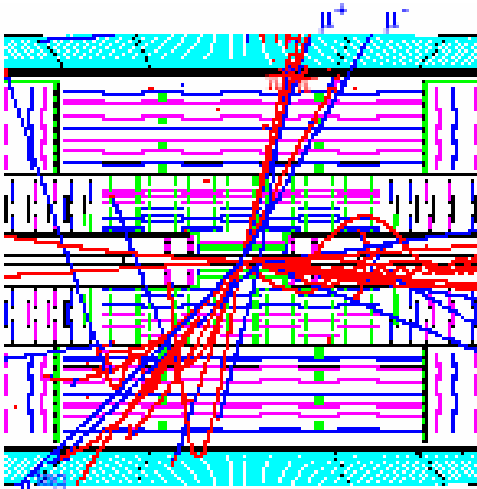
Compensate delays at front-end PLLs around ring

- All fibre lengths to FEDs including TTC fibre must be known
- Fibre lengths to/from FEC and cables around ring good to know but not critical

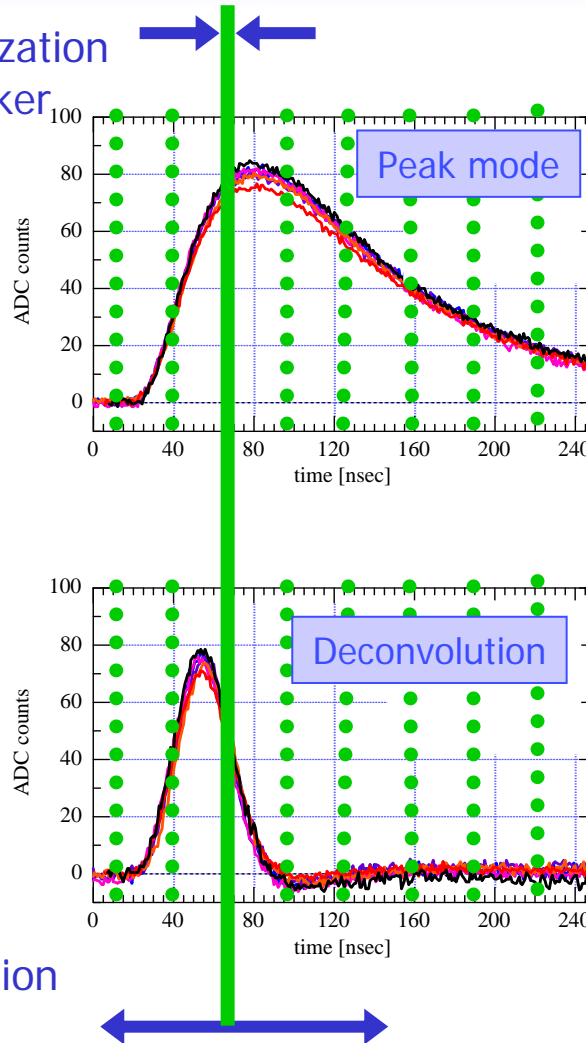


# Absolute synchronization

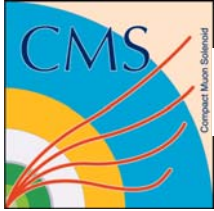
Relative synchronization  
to set up the Tracker



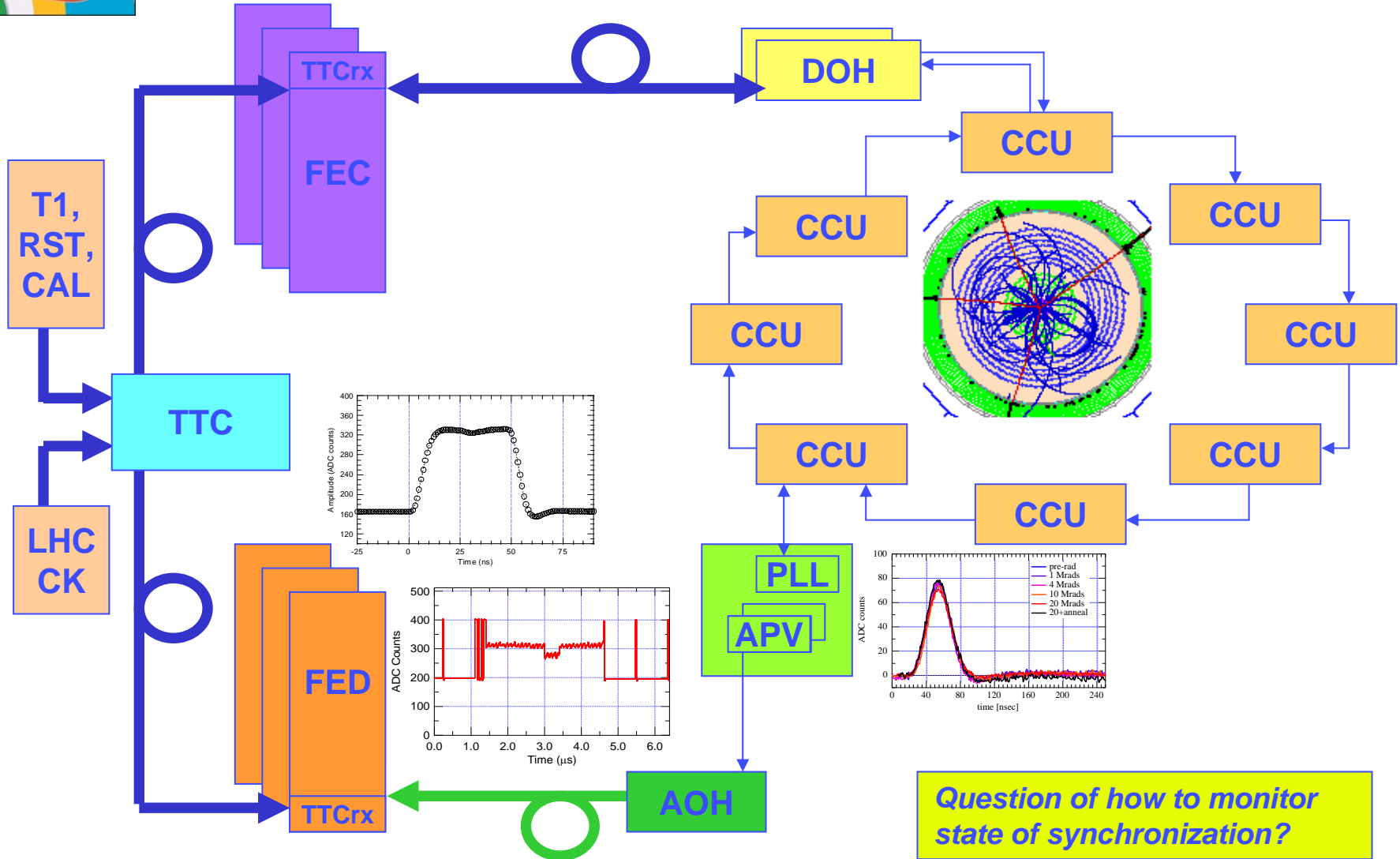
Absolute synchronization  
to find the particles...



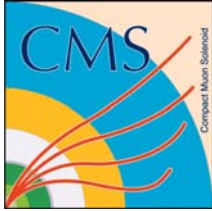
- *Relative synchronization aligns APVs with respect to one another*
  - *but not to LHC collisions*
  - *or rest of CMS*
- *Need to align APV sampling to signal generated in silicon strips by passing particles*
  - *Coarse timing*
    - *adjust latency at APV*
  - *Fine timing*
    - *re-adjust PLLs at front-end*
- *Same procedure used in beam-tests*



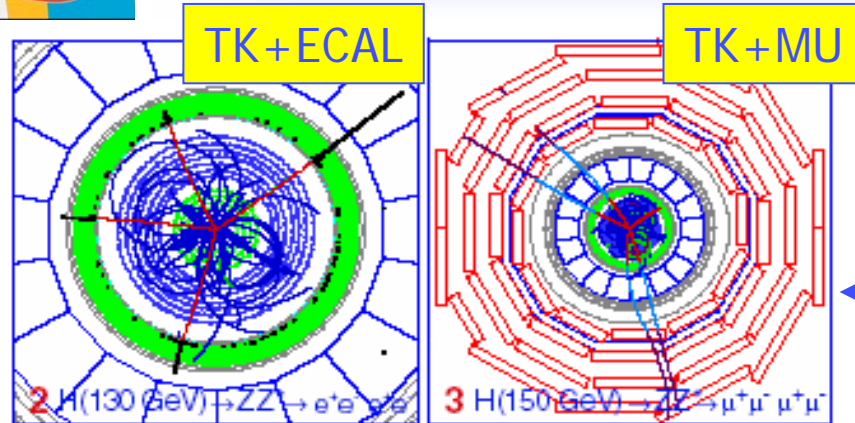
# Synchronization summary







# Monitoring synchronization



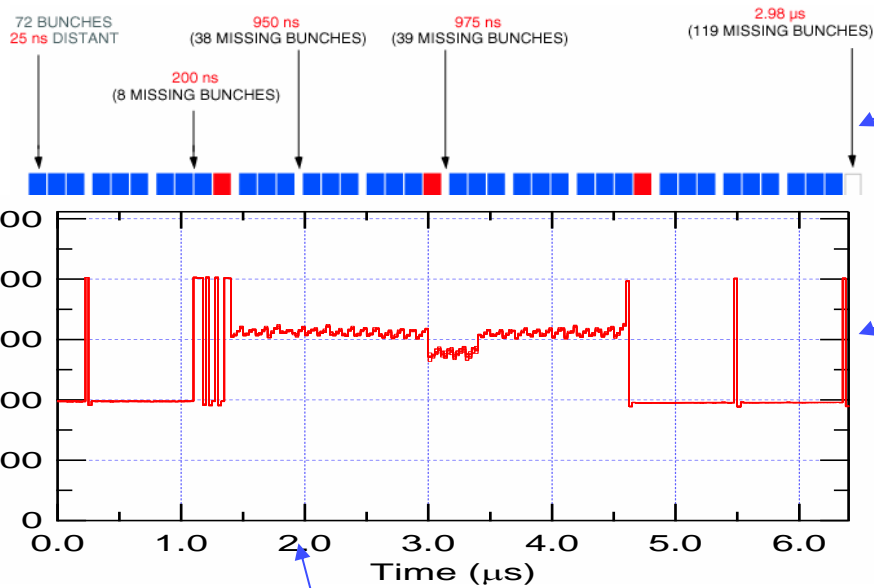
Synchronization may be lost for example due to a SEU damaging the PLL delay or APV latency register content

Tracker data should be consistent with rest of CMS

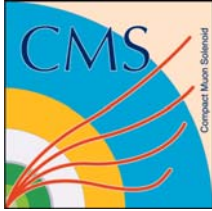
FED occupancy should match LHC bunch structure

FED compares 8-bit APV pipeline address in header on every channel with the expected one

- Coarse timing defects easy to see
- Fine setting problems more difficult



Digital header



# CMS SST cost (kCHF)

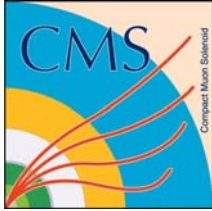
<i>Si detector production</i>	<b>29 284</b>
<i>Sensors</i>	<b>20 952</b>
<i>Kapton</i>	547
<i>Frames</i>	1 703
<i>Pitch adapters</i>	1 055
<i>FE hybrid</i>	2 065
<i>Tooling and box</i>	311
<i>ICB, CCUM, DOH and bus</i>	1 628
<i>Module pre-series</i>	1 023
<b><i>Electronics</i></b>	<b>21 578</b>
<i>Module electronics</i>	2 980
<b><i>Analog link</i></b>	<b>10 494</b>
<i>Digital link</i>	663
<i>Analog opto-hybrid</i>	847
<i>Digital opto-hybrid</i>	44
<b><i>FED</i></b>	<b>6 330</b>
<i>FEC</i>	220
<b><i>Power</i></b>	<b>8 600</b>
<i>Power supplies</i>	5 742
<i>Cables installed</i>	2 858

<b><i>Mechanical structures &amp; general cooling</i></b>	<b>9 936</b>
<i>Inner barrel</i>	1 033
<i>Inner Endcap</i>	358
<i>Outer barrel</i>	572
<i>Outer barrel rods</i>	1 220
<i>Endcaps</i>	1 260
<i>Endcaps petals</i>	905
<i>General cooling</i>	2 348
<i>Integration (st, ts, ...)</i>	2 240
<b><i>Monitoring</i></b>	<b>950</b>
<i>Position Monitoring System</i>	600
<i>Temperature</i>	350
<b><i>Data Acquisition</i></b>	<b>1 680</b>
<i>Test stands</i>	1 680
<b><i>Installation Manpower</i></b>	<b>1 000</b>
<i>Installation Manpower</i>	1 000
<b><i>Total</i></b>	<b>73 028</b>
<b><i>Deficit</i></b>	<b>-4 950</b>

**Beware:** costing does not include

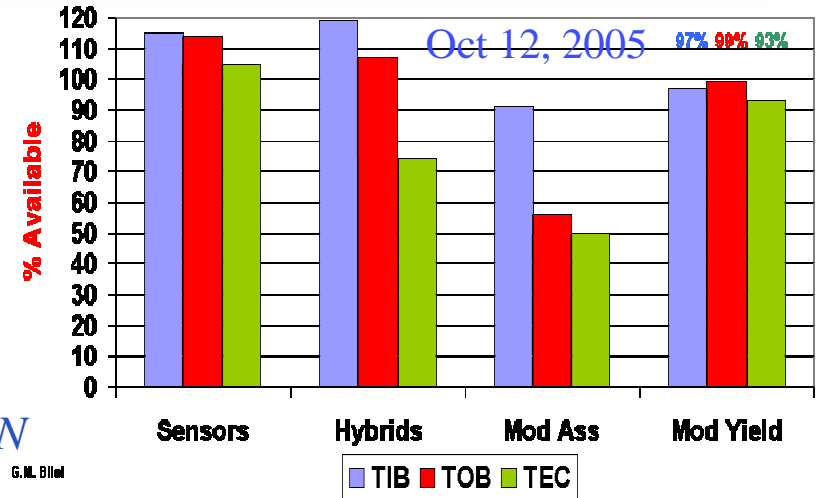
- Spares (~2.5 MCHF)
- Labor (provided by different agencies in different forms, impossible to estimate)
- Local exploitation budgets (often used for every day work, believed not to be a big correction)
- M&O
- TDR Addendum (2000 Feb) SST cost estimate: 69 343 kCHF

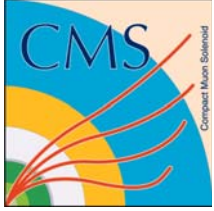
- Pixel: 8.2 MCHF (estimate not updated since a very long time, expected to move up)



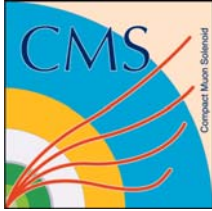
# Summary

- *TOB rod production re-started*
- *50/288 TEC petals built*
- *Integration process started*
  - *TIB L3+, L4+ complete*
  - *Tracker Support Tube commissioned*
  - *TOB support structure complete*
  - *TEC+ support structure (almost) complete*
  - *TEC – being prepared for transport to CERN*
  - *Trial integration successful*
- *The quality of substructure is found to be excellent both in system tests and in test beam experiments*
- *Full Tracker integration is about to start*
- *Commissioning by readout tests of 25% of the Tracker with final DAQ system is expected from March 2006*
- *We have a rather bumpy road with many adventures toward the completion of CMS SST and I am sure new challenges are ahead us*
- *It is very demanding to deliver a tested SST to LHC Point 5 by November 2006... but we (hopefully) learned from past problems and mistakes and are motivated*
- *The high energy physics community should draw conclusions and avoid to make the same mistakes at ILC*





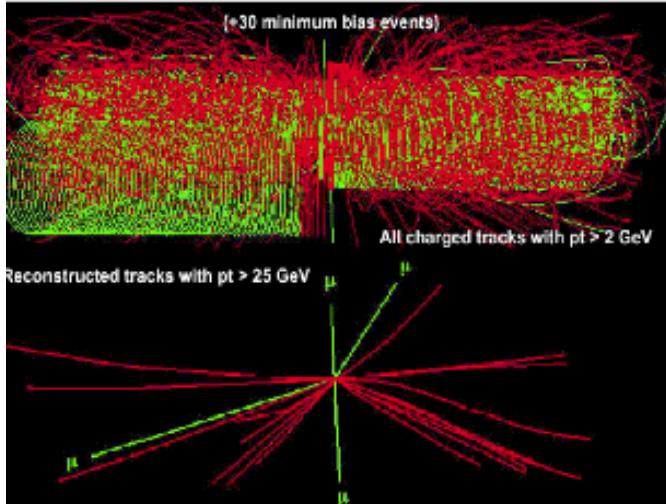
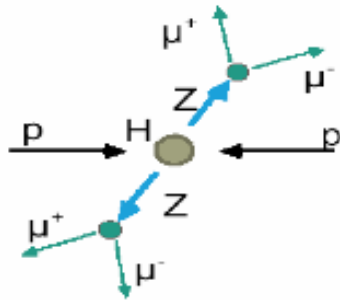
# *Backup Slides*



# Physics requirements for Tracking

The quest for Higgs driven the design of LHC detectors to a large extent but other physics of interest (SUSY, CP-violation...) require the same capabilities

"Golden Channel"



## Efficient tracking

- Fine granularity to resolve nearby tracks
- Fast response time to resolve bunch crossings
- Number of layers to recognise trajectories

Reconstruct high  $p_T$  tracks and jets for precise reconstruction of heavy narrow particles

- High magnetic field
- Large lever arm (alignment of inner Tracker and Muon system)

- $\sigma(p_T) = 1-2\%$  for 100 GeV muons

Reconstruct low  $p_T$  tracks (down to 1-2 GeV) for effective isolation criteria

$b$  and  $\tau$  tagging (for new physics, top tagging, CP violation)

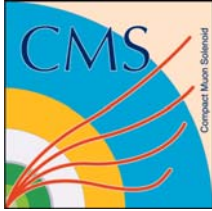
- Impact parameter resolution  $\sim 10-20 \mu\text{m}$

## Minimal perturbation

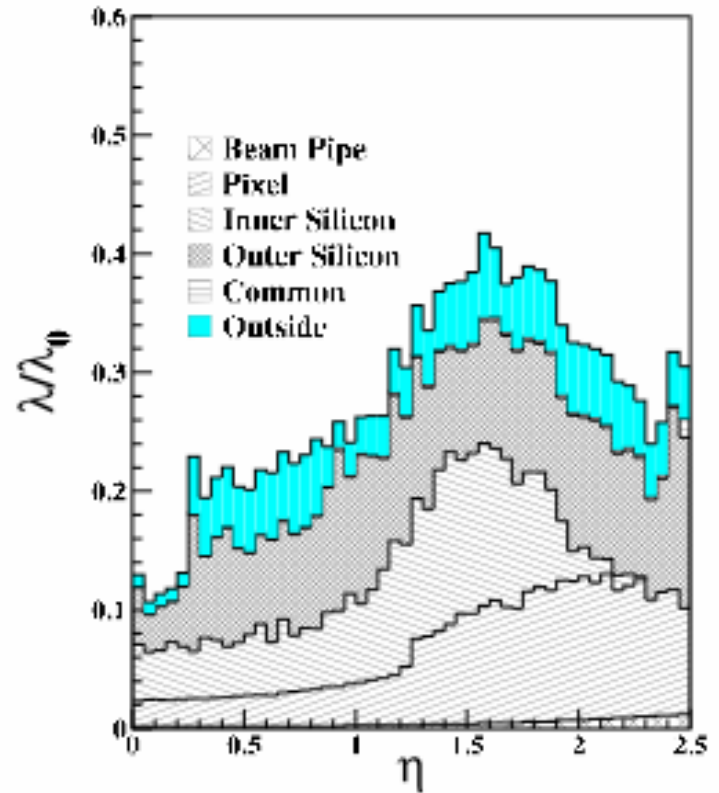
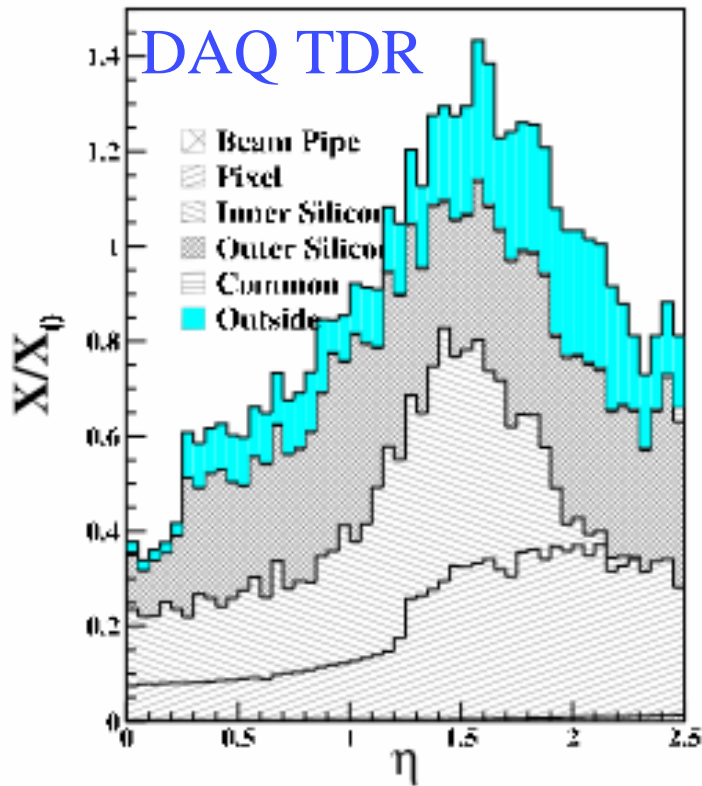
- Minimise material, low Z desirable (multiple scattering, hadronic interactions, effects on electron and photon detection and isolation due to brehmstarhlung and conversion)

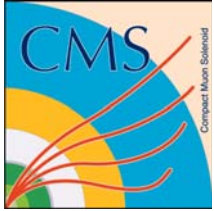
Radiation hard

Cost



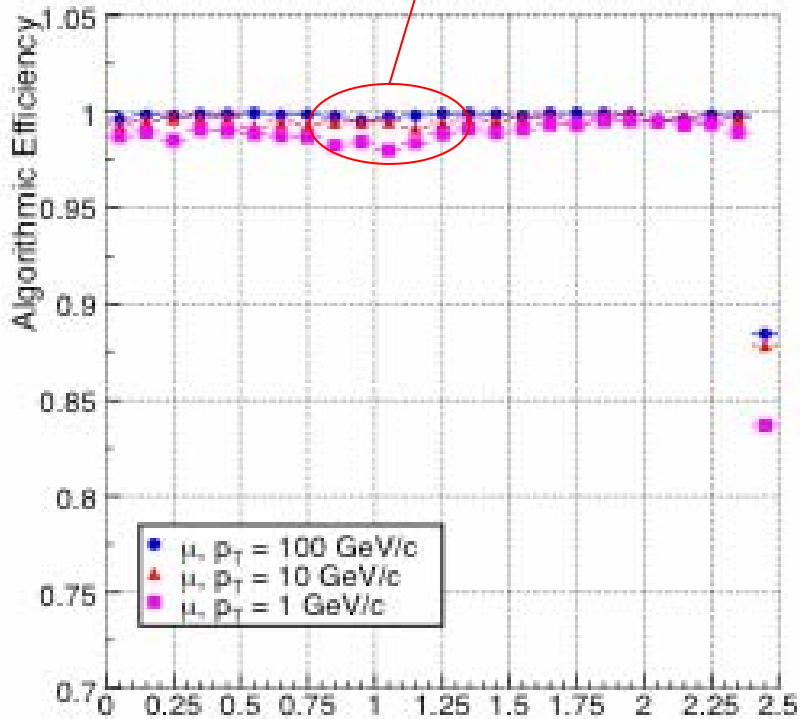
# Material budget





# Track reconstruction efficiency

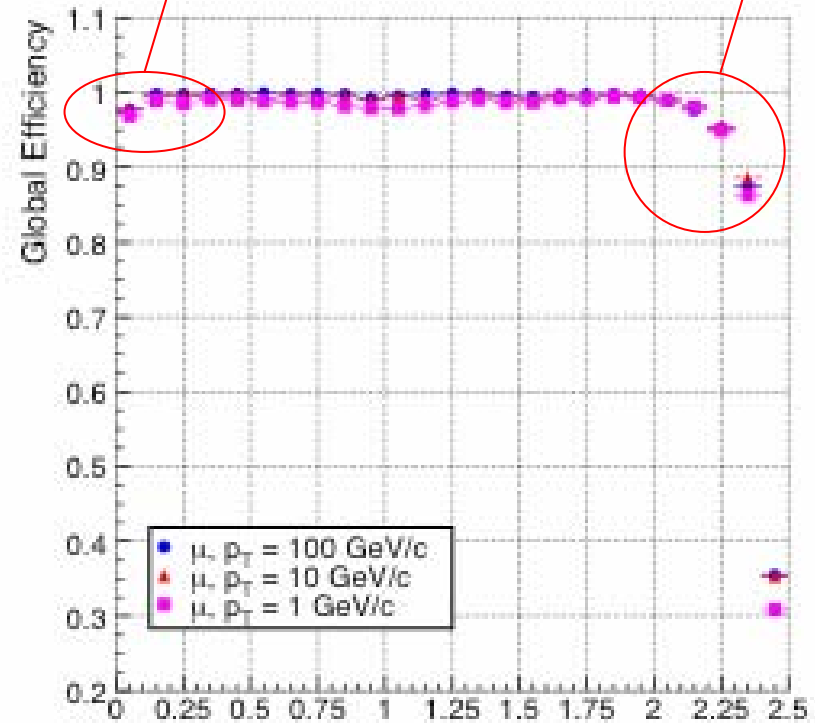
Gap between barrel and endcap systems



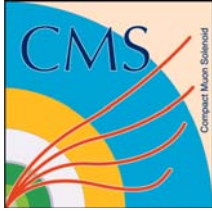
$\eta$

Gap between two half barrels of pixel

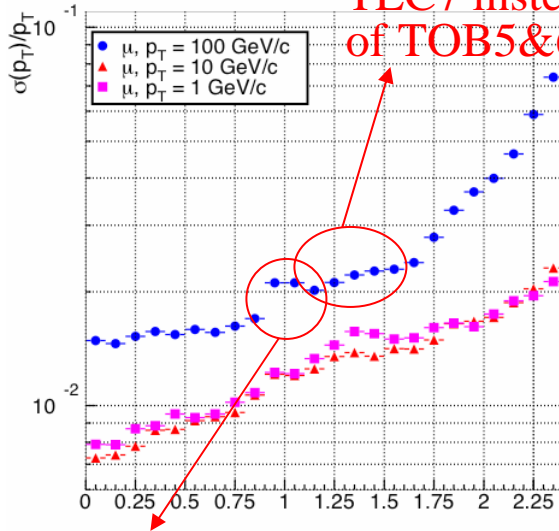
Loss of endcap disk coverage



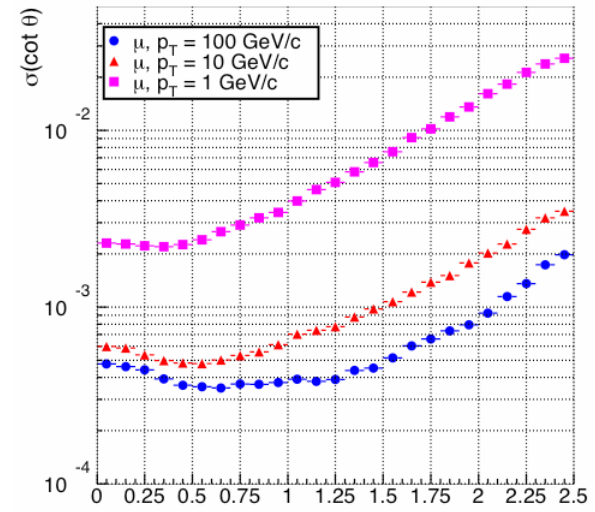
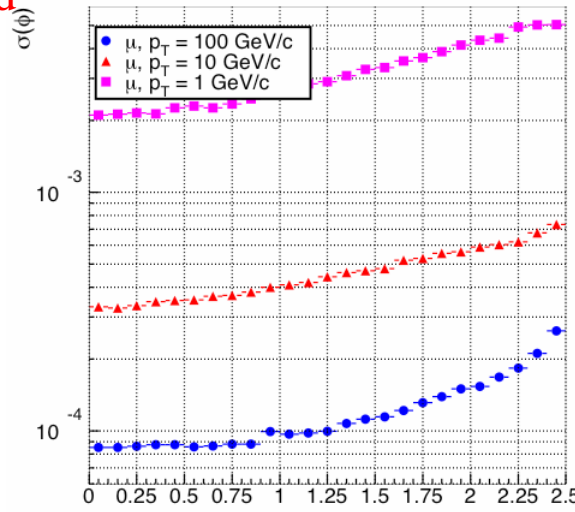
$\eta$



# Track parameter resolutions

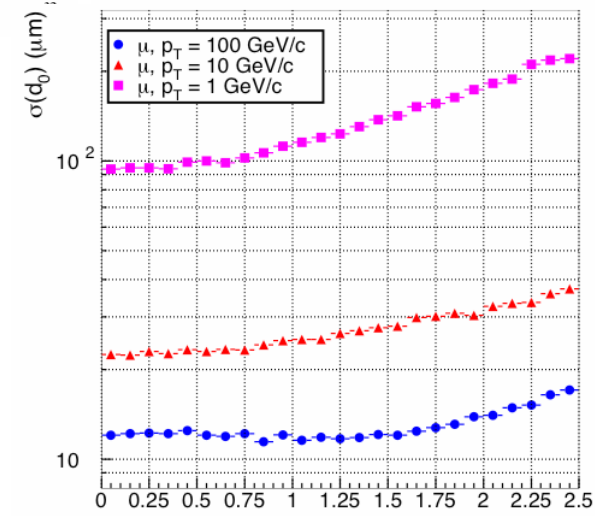
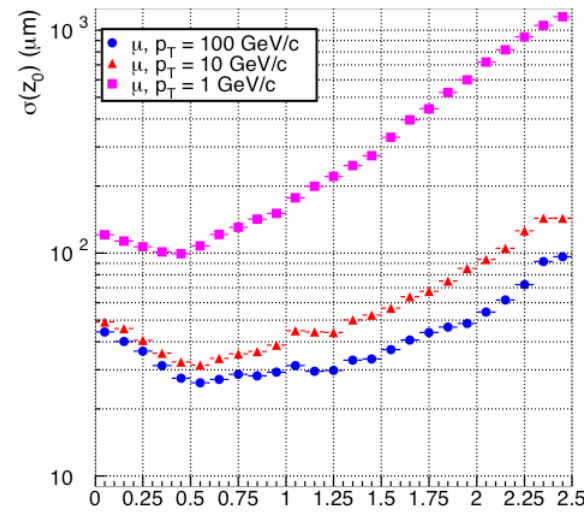


TEC7 instead of TOB5&6

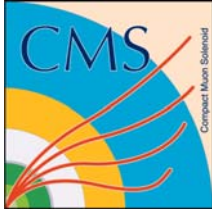


Gap between barrel and endcap systems

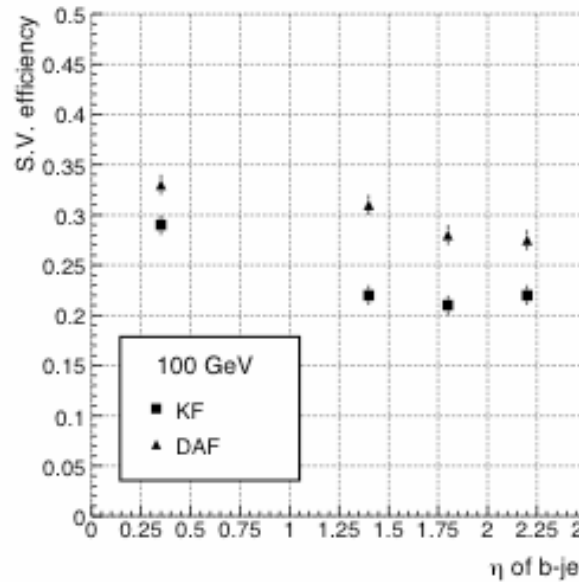
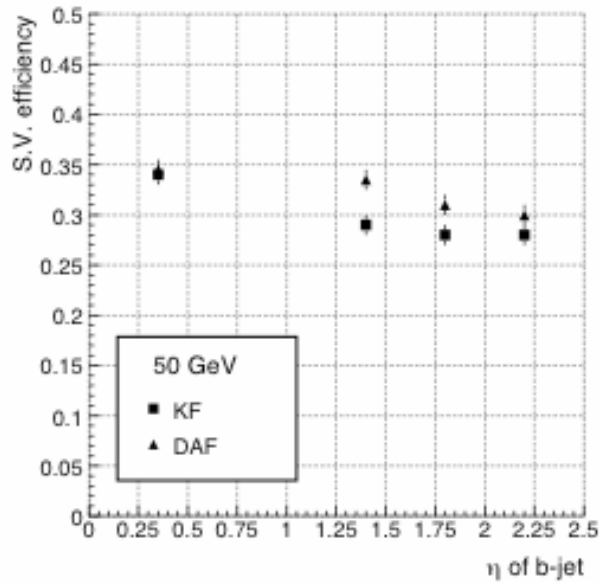
- Low momenta: dominated by multiple-scattering
- 100 GeV track: 20-30% of  $\sigma(p_T)$  comes from tracker material



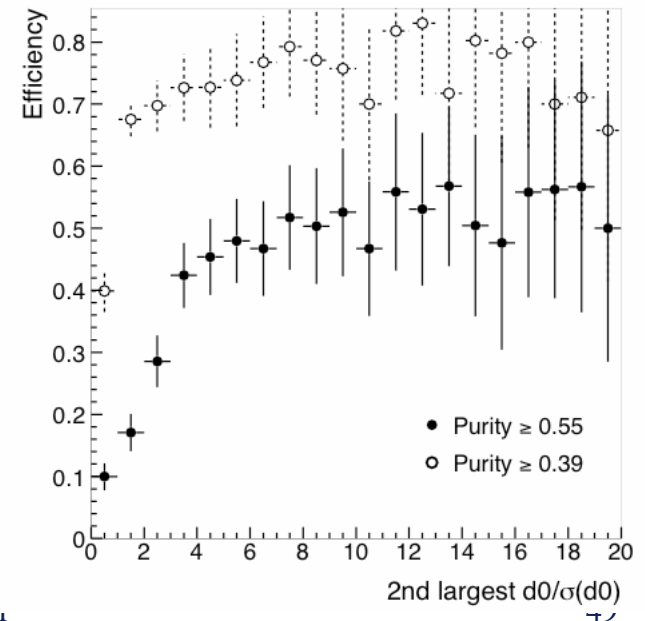


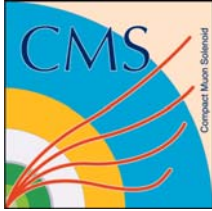


# Secondary vertex finding efficiency inside a b-jet

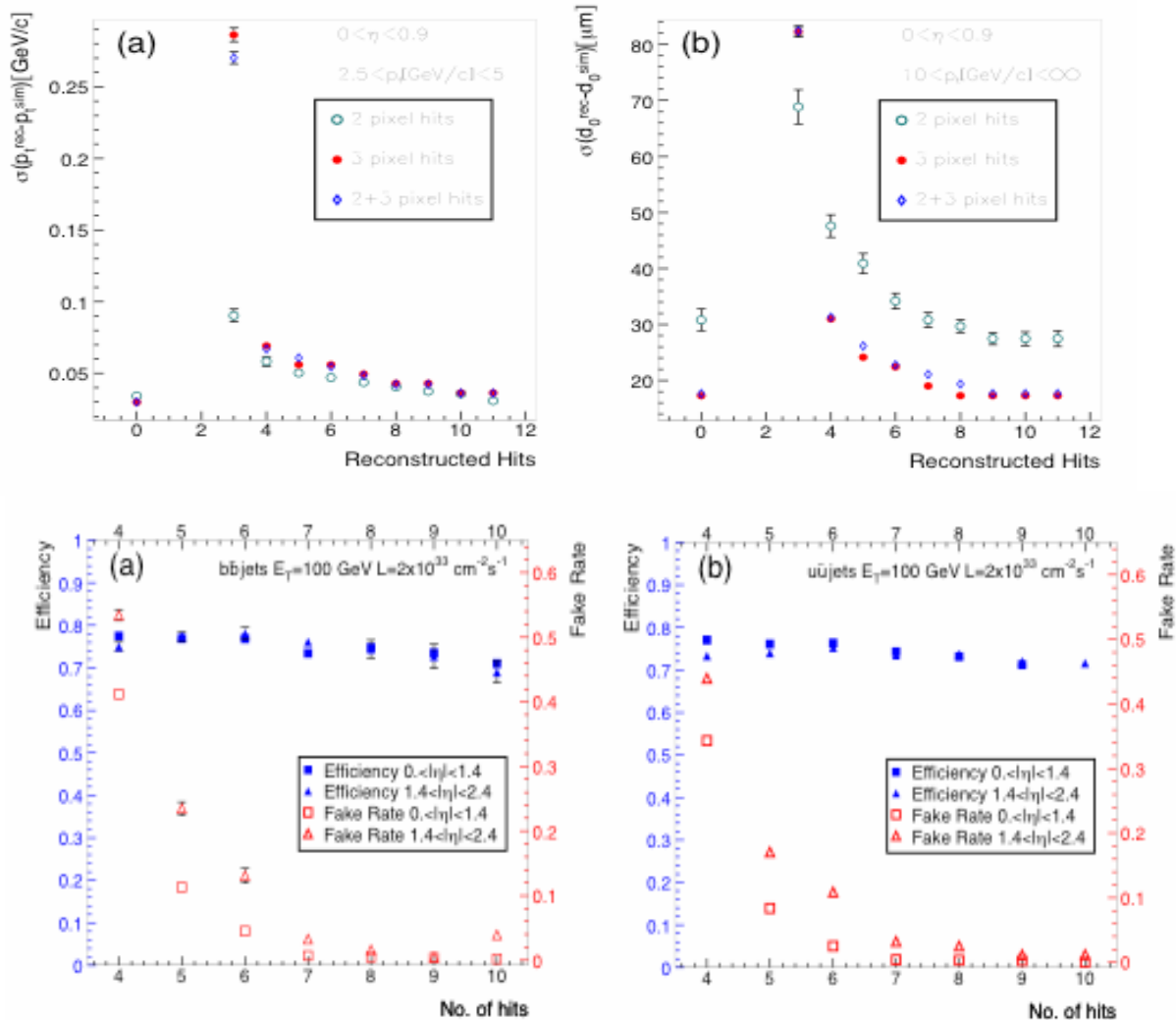


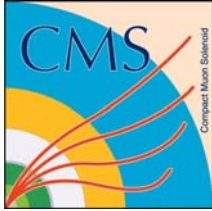
Secondary vertex finding efficiency as a function of the impact parameter significance of the track with the 2nd largest impact parameter





# Partial track reconstruction

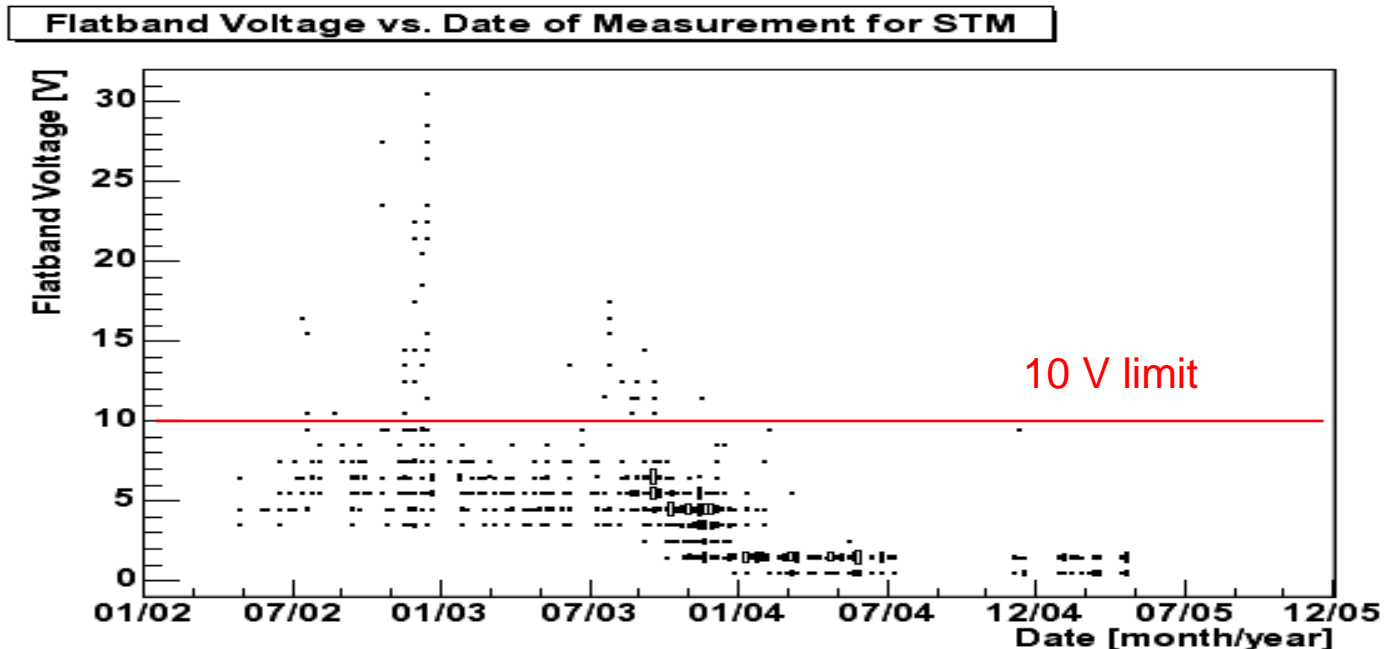


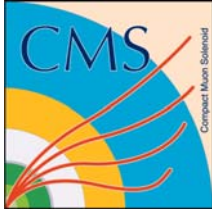


# *Problems during mass production*

Too high **flatband voltage** for some STM batches

- A consequence of a high flatband voltage is a large increase of the interstrip capacitance after irradiation
- The failure was traced back to a contamination of a single machine in the production line

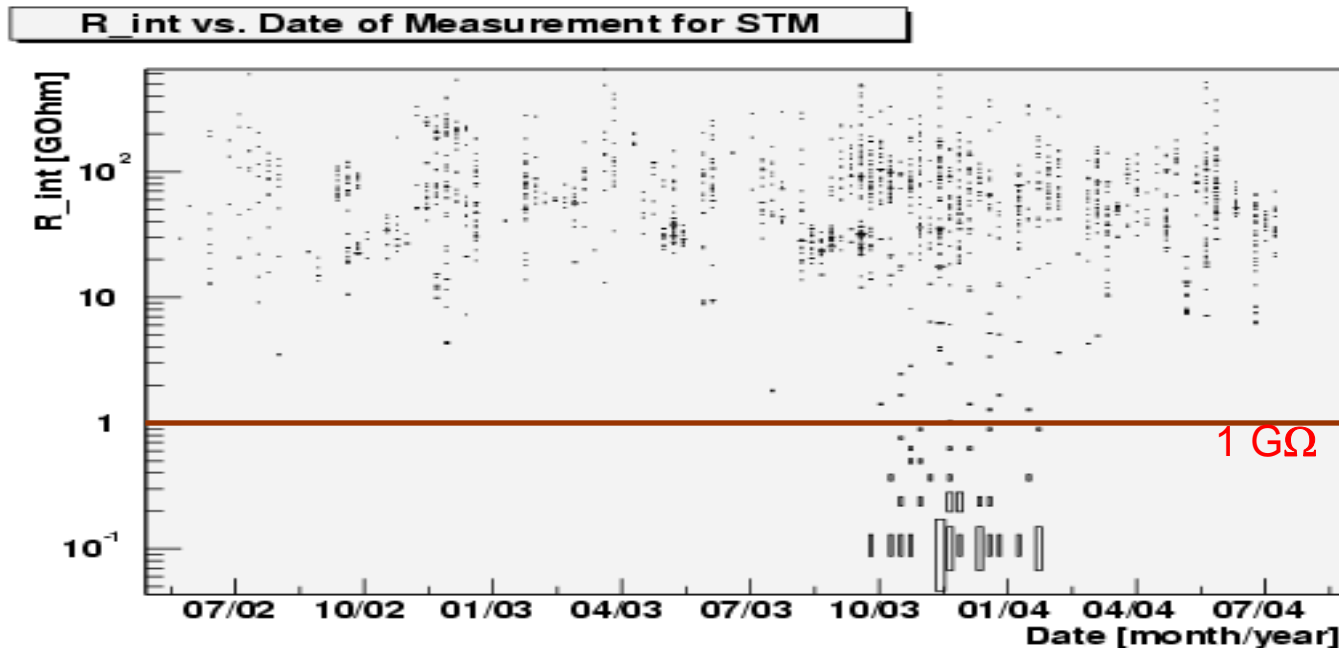


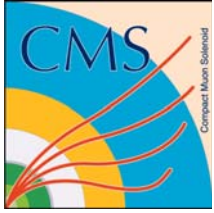


# *Problems during mass production*

Too low **interstrip resistance** for some STM batches

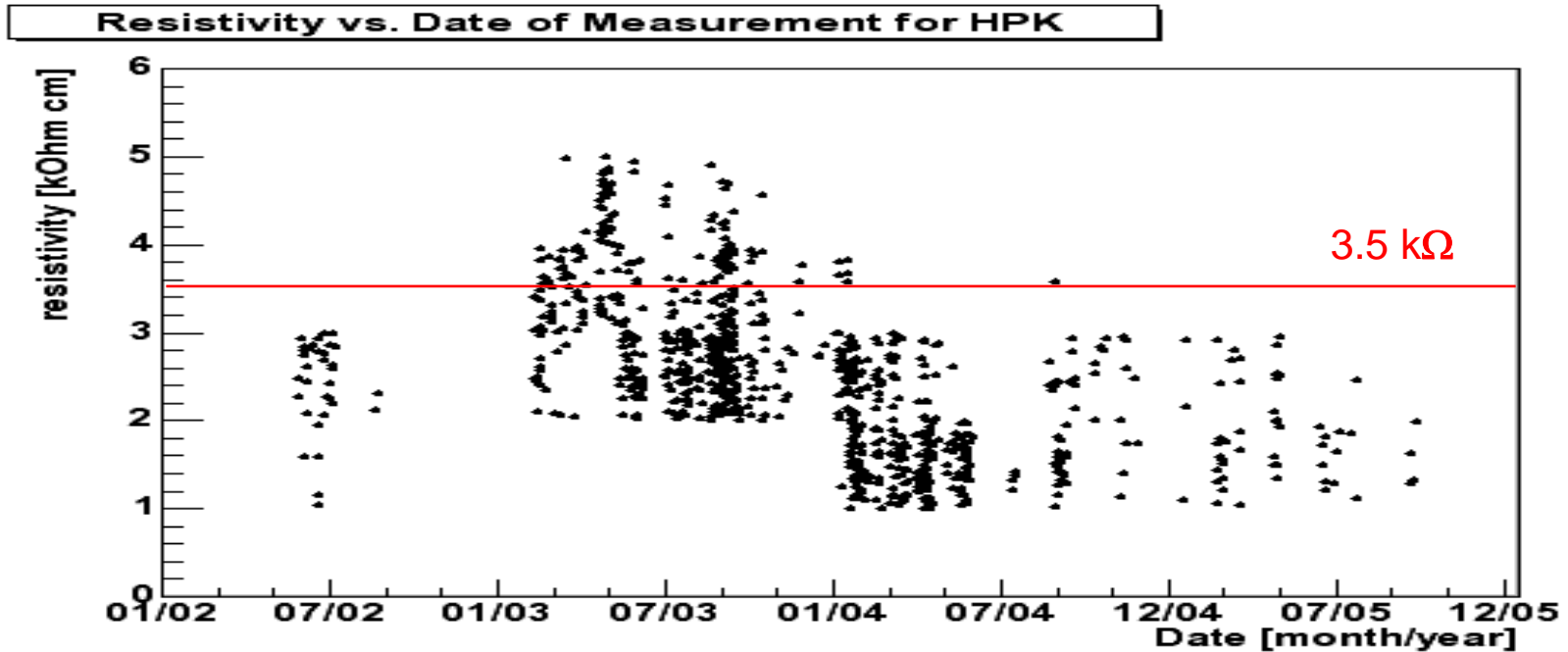
- The limit is  $1\text{ G}\Omega$  to ensure good electrical separation of adjacent strips
- The failure was traced back to a small parameter variation in the production line

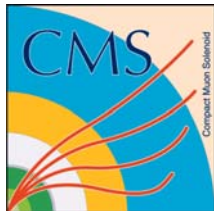




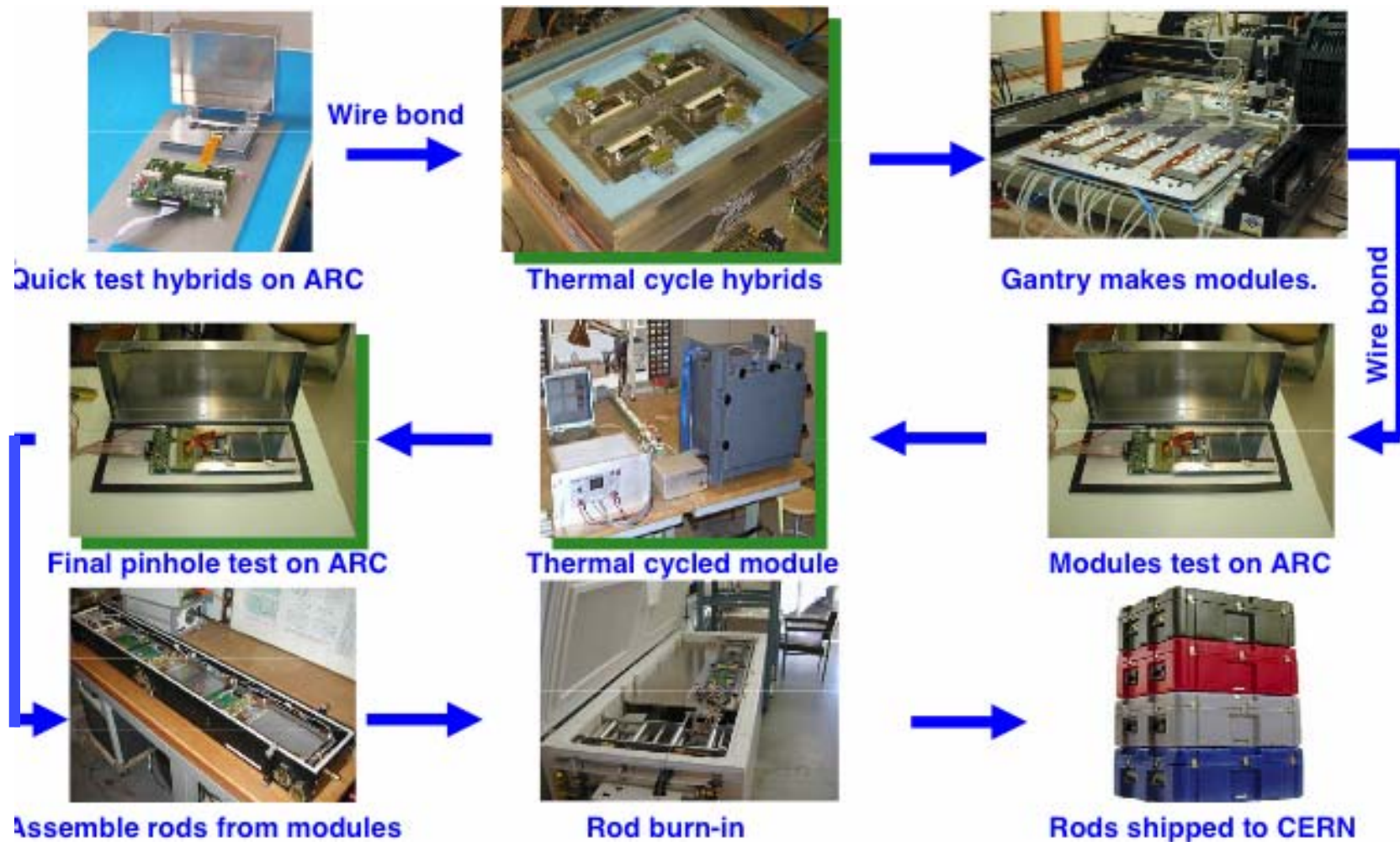
# *Problems during mass production*

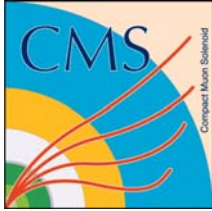
Thin sensors (HPK) produced during a certain period with wafer material of too high resistivity





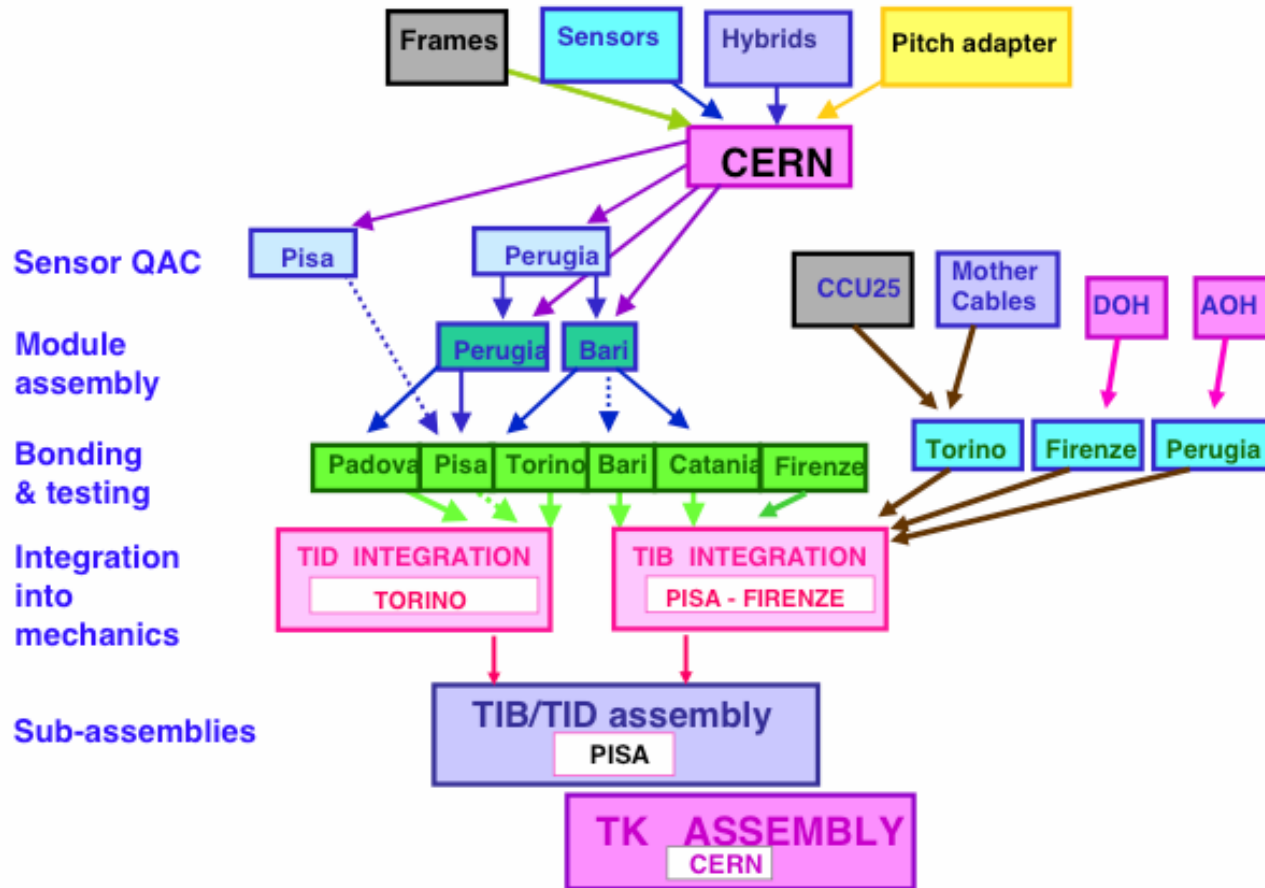
# *TOB module - rod production cycle*

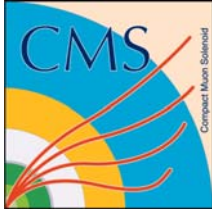




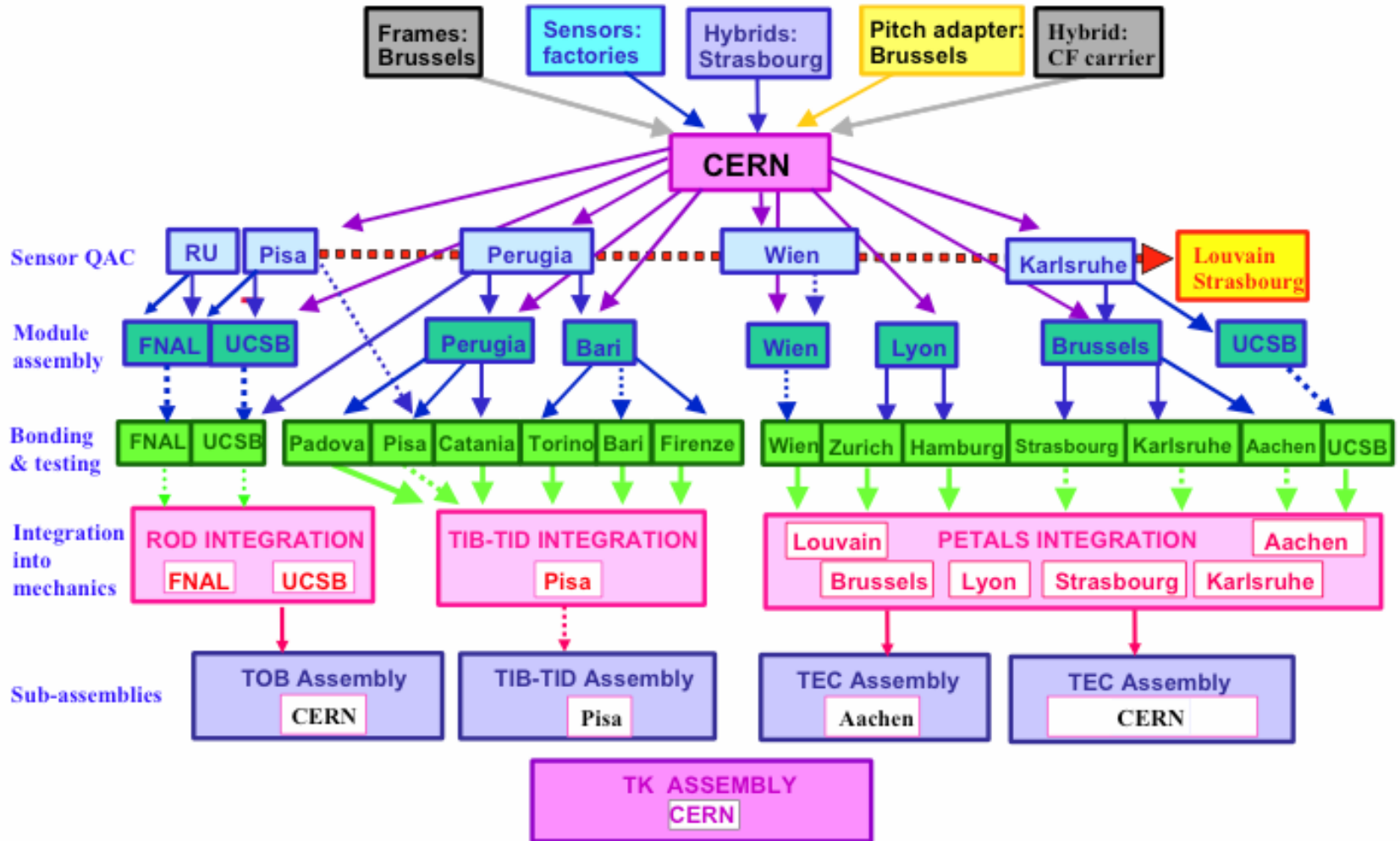
# TIB/TID logistics

## TIB/TID General Organization

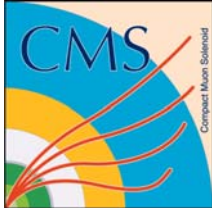




# Tracker logistics





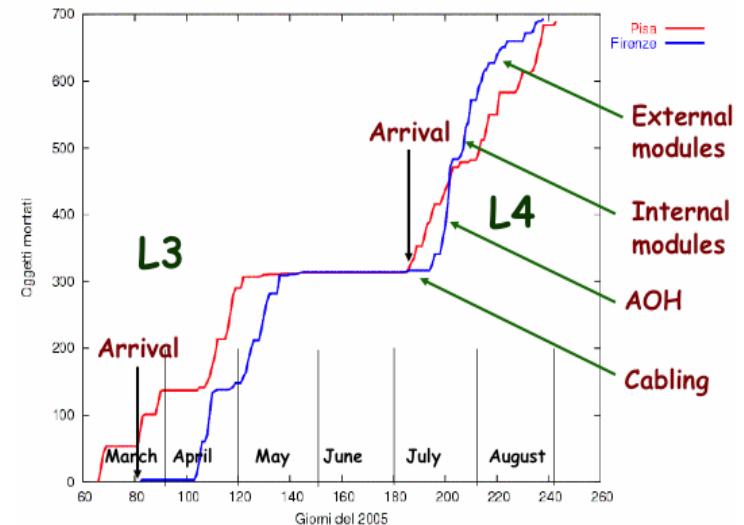


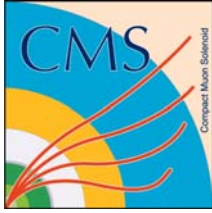
# TIB layer integration

## Integration procedure

- Mounting of **Analog Optohybrids** (and fibers)
- Cabling of **Digital Opto Hybrids** and ring test (also redundancy)
  - Digital Optohybrid Modules already tested in Florence
  - Mother Cables and CCU25 already tested in Torino
- Mounting of **Modules**
  - HV and readout test for every string (3 modules)
- Final mounting of **Digital Opto Hybrids** (again ring test)
- All components and test results stored in integration **DB**
- The structure is moved to burn-in test

## How long we needed to integrate Layer 3 & 4 ?





# TIB performance during integration and burn-in

Test performed during integration

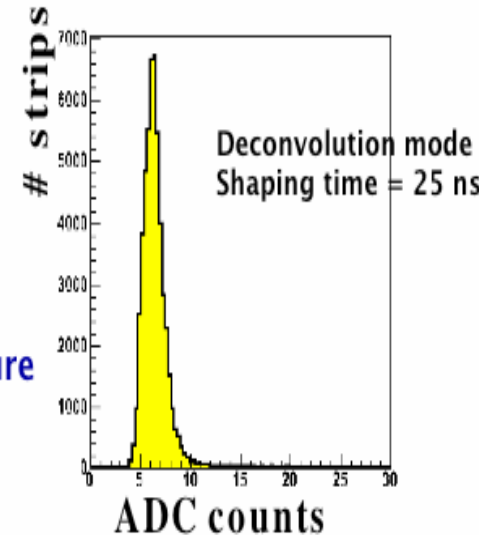
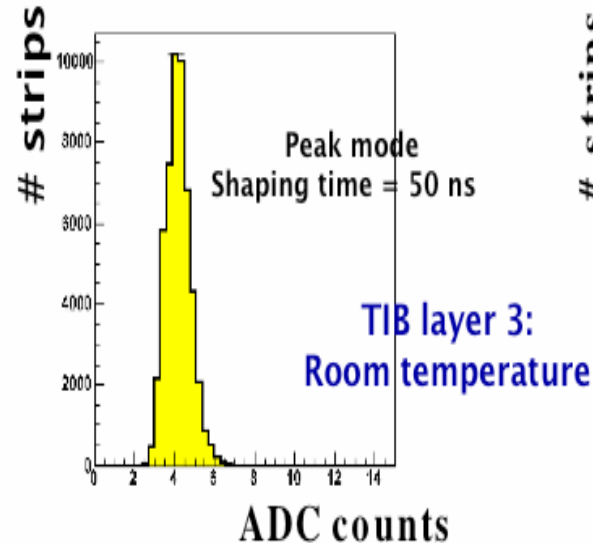
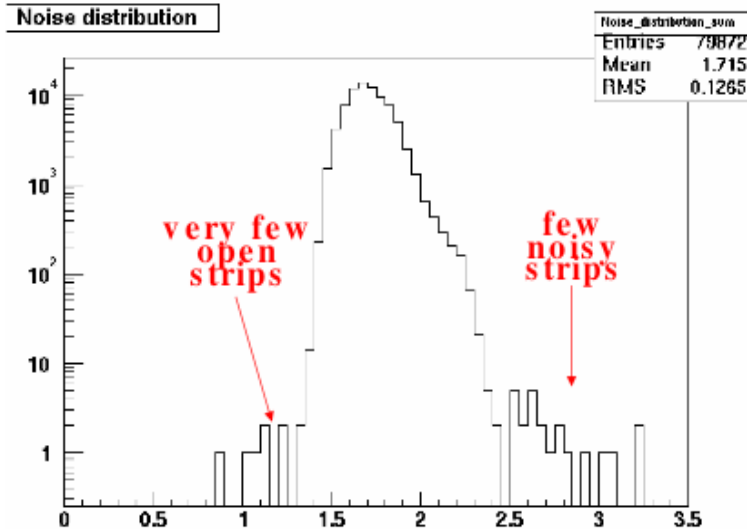
- I<sup>2</sup>C communication
- Control ring communication and redundancy
- Readout per string using prototype readout system

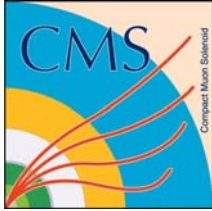
After integration, each half cylinder undergoes a complete validation test (burn-in) using final components (power supplies, cables, DAQ for readout of full half cylinder) including thermal cycles from -25 to +10 C

Layer 3 burn-in successfully completed

Single strip noise in deconvolution mode (all mounted TIB layer 4 modules)

Noise distribution





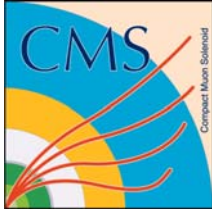
# Alignment strategy

- *Assembly tolerances and measurements*
  - *Mechanical accuracy to position individual silicon modules within the subdetectors (TIB, TOB, and TECs) is about 50  $\mu\text{m}$*
  - *Mechanical accuracy between the subdetectors likely be  $\sim$  a mm*
- *Laser alignment system for large structures (not for TID and Pixels):*
  - *Monitors the tracker support structure (each module can not be monitored) and generates alignment info to correct the tracker data online*
  - *Provides the required absolute detector positional accuracy of 100  $\mu\text{m}$  for the stable operation of the pattern recognition system, needed for HLT*
- *Track-based alignment (online TBA for Pixels and full TBA offline with high statistics)*
  - *Achieves the ultimate measurement accuracy of  $\sim$ 10  $\mu\text{m}$ , needed for offline track parameter and vertex reconstruction*
  - *Uses sufficient statistics of reconstructed tracks (several days of data taking)*

*Expected RMS values after mechanical constraints and laser alignment:*

	TPB [ $\mu\text{m}$ ]	TIB [ $\mu\text{m}$ ]	TOB [ $\mu\text{m}$ ]	TPE [ $\mu\text{m}$ ]	TID [ $\mu\text{m}$ ]	TEC [ $\mu\text{m}$ ]
Modules	13	200	100	2.5	105	50
Ladders/Rods/Rings/Petals	5	200	100	5	300	100

	$\Delta x$ [ $\mu\text{m}$ ]	$\Delta y$ [ $\mu\text{m}$ ]	$\Delta z$ [ $\mu\text{m}$ ]	$R_z$ [ $\mu\text{rad}$ ]	LAS available
TPB	10	10	10	10	no
TIB	105	105	500	90	yes
TOB	67	67	500	59	yes
TPE	5	5	5	5	no
TID	400	400	400	100	no
TEC	57	57	500	46	yes



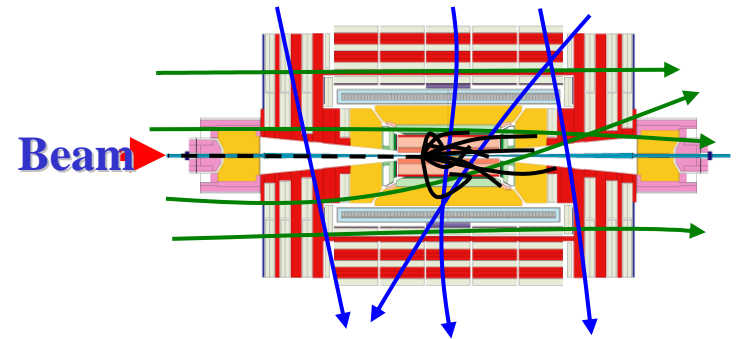
# Alignment with tracks

Before physics runs:

- High energy cosmic muons traversing vertically (60 Hz in Tracker for  $E > 10$  GeV, barrel region)
- Beam Halo muons (hadrons) crossing almost horizontally (200 Hz for  $E > 100$  GeV, endcap region)
- Beam-gas interactions resembling collision events with a very soft  $p_T$  spectrum  $E \sim 115$  GeV (large rate at single beam operation)

During data taking:

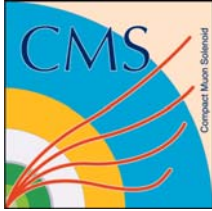
- Generic tracks, isolated muons e.g. from  $W$  decays,  $Z \rightarrow \mu\mu$



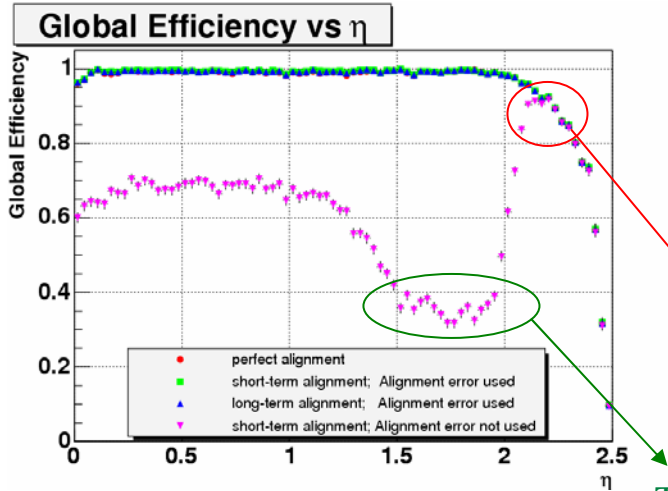
Expected RMS values after  $10 \text{ fb}^{-1}$

	TPB [ $\mu\text{m}$ ]	TIB [ $\mu\text{m}$ ]	TOB [ $\mu\text{m}$ ]	TPE [ $\mu\text{m}$ ]	TID [ $\mu\text{m}$ ]	TEC [ $\mu\text{m}$ ]
Modules	13	20	10	2.5	10.5	5
Ladders/Rods/Rings/Petals	5	20	10	5	30	10

	$\Delta x$ [ $\mu\text{m}$ ]	$\Delta y$ [ $\mu\text{m}$ ]	$\Delta z$ [ $\mu\text{m}$ ]	$R_z$ [ $\mu\text{rad}$ ]
TPB	10	10	10	10
TIB	10.5	10.5	50	9
TOB	6.7	6.7	50	5.9
TPE	5	5	5	5
TID	40	40	40	10
TEC	5.7	5.7	50	4.6



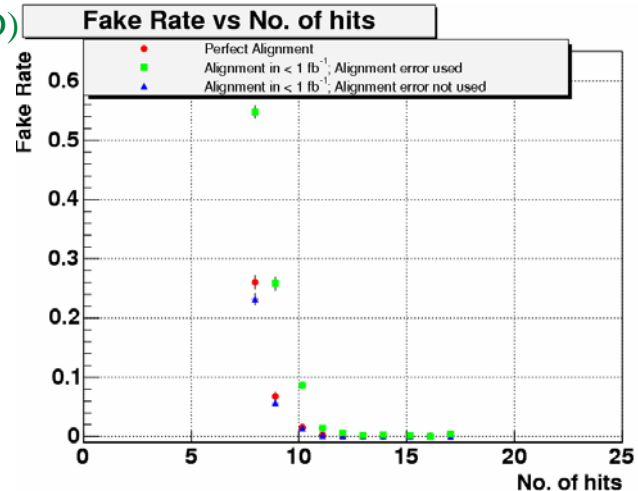
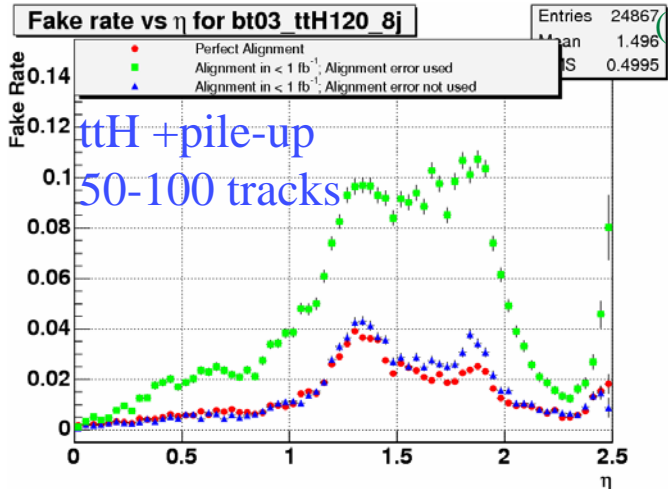
# Track based alignment

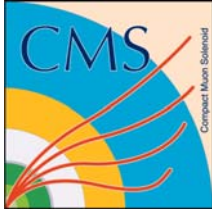


- Enlarge errors assigned to the reconstructed hits by  $\sigma_{\text{alignment}} = \sigma_{\text{superstruct}} \oplus \sigma_{\text{struct}} \oplus \sigma_{\text{module}}$  at track fitting
- Increases fake rate significantly but tracker redundancy is enough to keep this still at an acceptable level

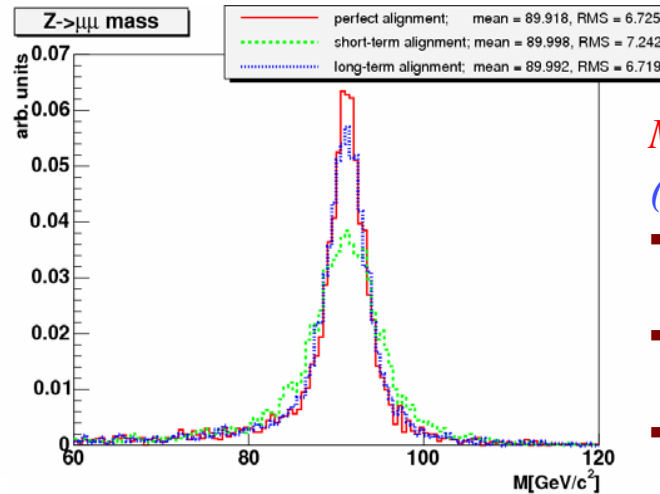
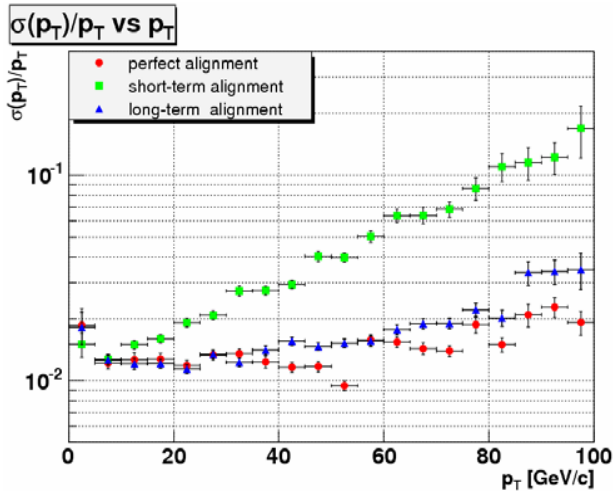
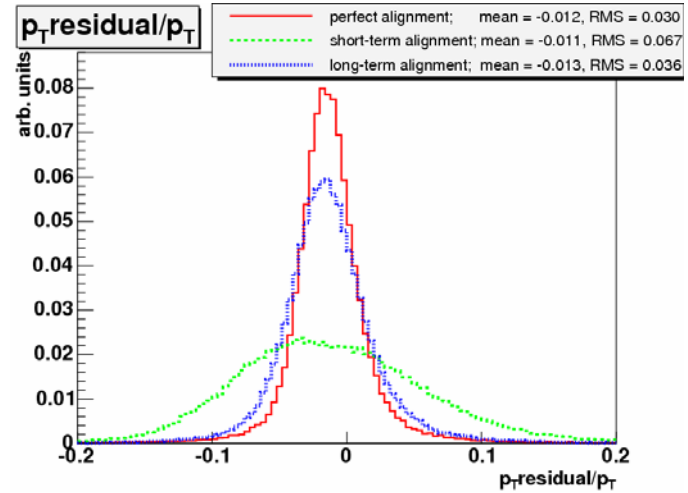
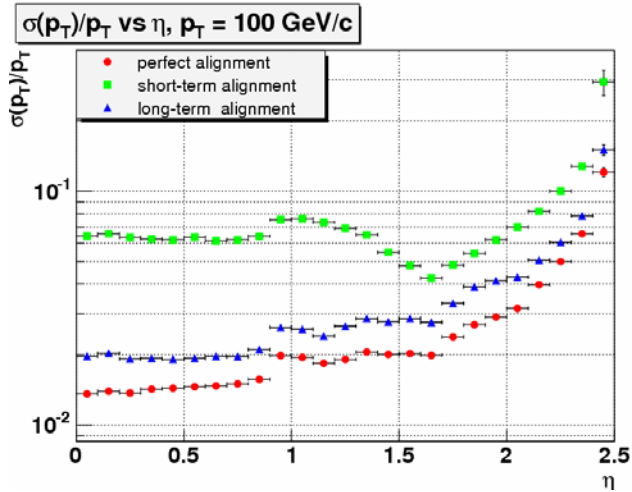
TID (with large mis-alignment) not used  
(Pixel Endcap, TEC)

TOB not used  
(Pixels, TIB, TID)

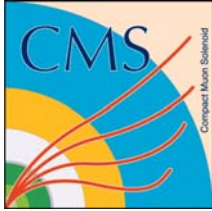




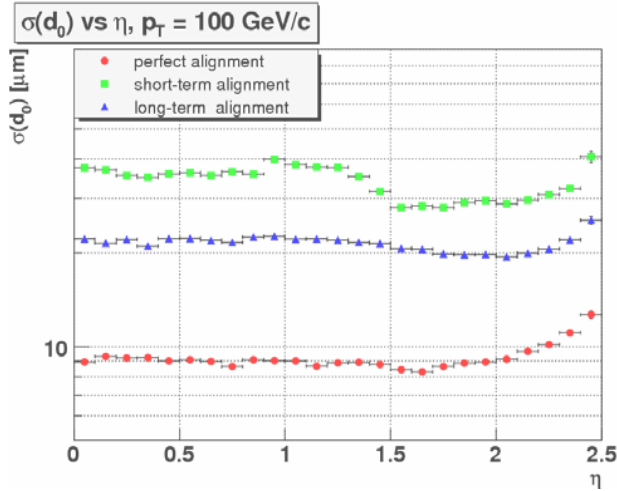
# Effect of mis-alignment



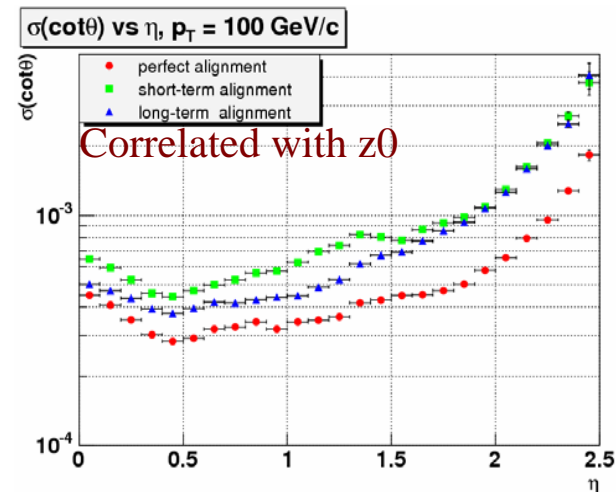
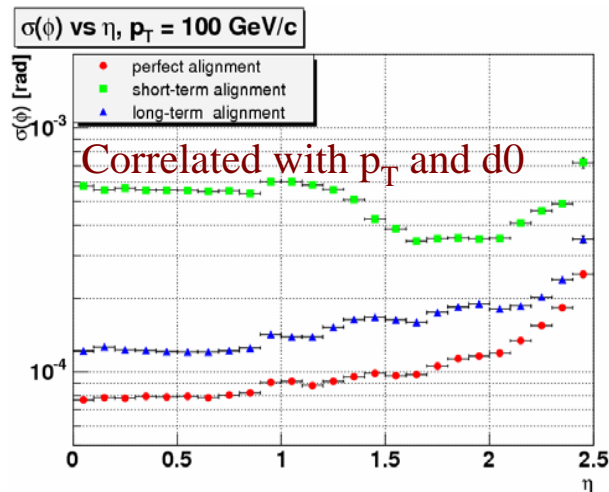
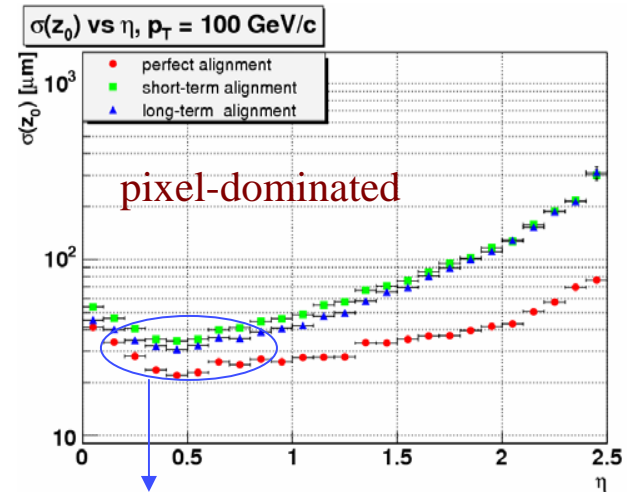
- Muons in  $H \rightarrow ZZ \rightarrow e e \mu \mu$   
( $m=300$  GeV) at low luminosity*
- *Mis-alignment affects high- $p_T$  more*
  - *Multiple scattering dominates below 10 GeV*
  - *Mass resolution  $\sim 10\%$  worse on short-term*

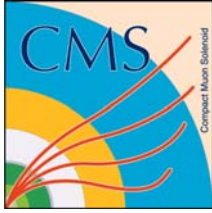


# Effect of mis-alignment



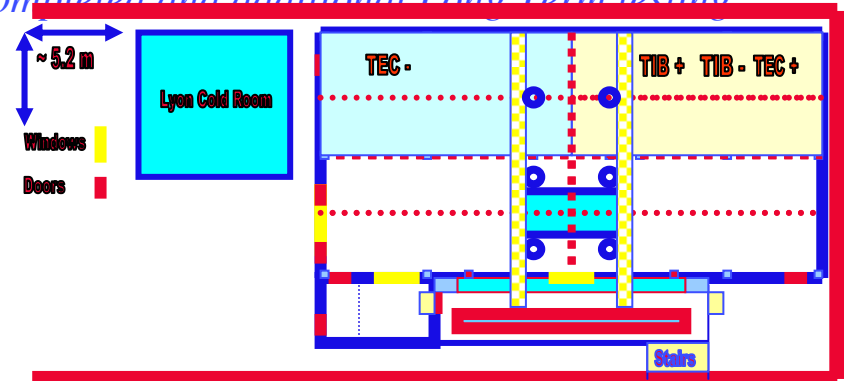
dominated by the hit resolution of the first hit in the pixel detector, long-term: factor  $\sim 2$  degradation



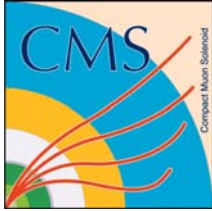


# Tracker Integration

- *Integration and Commissioning of the Tracker was reviewed last week*
- *Integrate all of the Tracker at CERN starting in November 2005 with an Integration and Commissioning Team of 60 people*
- *New Tracker Integration and Commissioning Facility (TIF) at CERN to be complete at the end of October*
- *The TIB + and TIB - sub-detectors will be integrated in Italy (Pisa and Florence) and then moved to CERN (TIB/TID+ in January 2006, TIB/TID- in April 2006), tested and inserted into TOB+ and TOB - .*
- *TOB + and TOB - will be integrated at CERN with Quality Assured rods fabricated in the USA (UCSB and FermiLab)*
- *TEC + will be integrated in Aachen and TEC - structure will be shipped to CERN in November and integrated there*
- *TEC is on critical path: to accelerate the TEC programme, TEC modules will be fabricated in Italy and the USA after TIB and TOB modules have been completed and additional Long Term testing facilities will be provided at CERN*
- *Have 'Standard' Electronics and DAQ Systems in November*
- *25% tests of the Tracker from March 2006 (cooling, power, readout, DAQ, DCS, cables)*

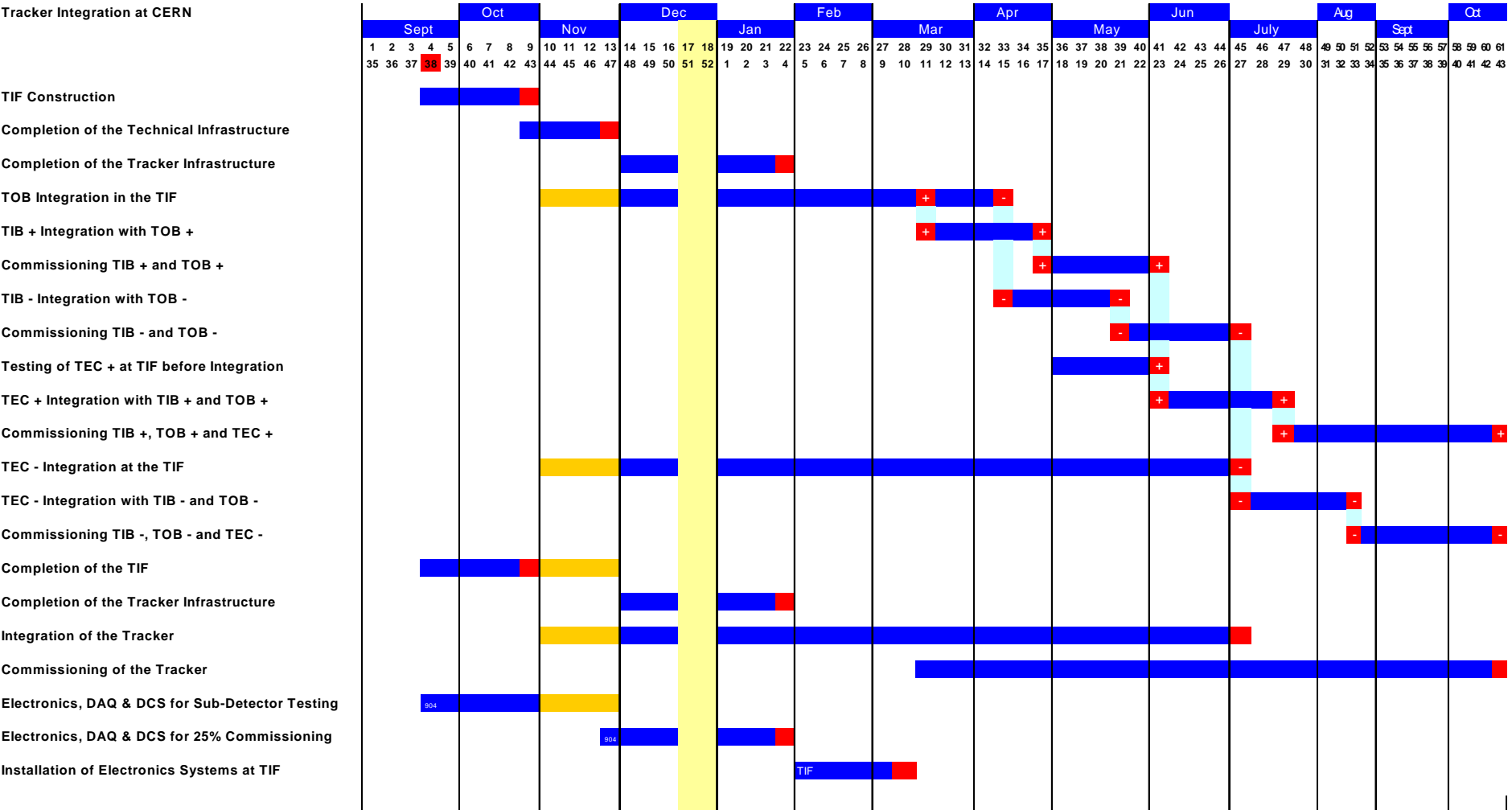


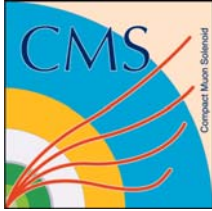




# Integration schedule

Tracker Integration at CERN



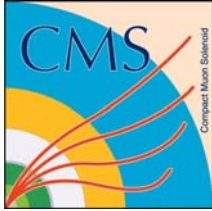


# Commissioning tasks

- *The Construction Project includes the Integration of the Tracker*
- *The Operations Project will involve commissioning the Tracker*
  1. *Safety and Interlock Systems*
  2. *Cooling Systems*
  3. *Power Supply Systems*
  4. *DCS and DAQ Systems*
  5. *Interfaces to the CMS Trigger Systems*
  6. *Database Systems*
  7. *Data Quality Monitoring*
  8. *Track and Vertex finding algorithms*
  9. *Alignment algorithms*
  10. *Interfaces to the physics analysis.*
- *Data Quality monitoring will be developed to monitor the performance of the Tracker from remote sites*



*In collaboration  
with Software Project*



# Commissioning

- *A first review of commissioning plans will be held in December*
- *A first review of the plans for installation at Point 5 will be held in the March 2006*
- *Initial commissioning of the Electronics, DCS and DAQ will take place in the CMS Electronics Integration Center (EIC) so that only complete working systems are transferred to TIF & USC55. This should be ready to transfer to the Integration Facility for the 25% tests in January, and be fully commissioned by March 2006. The remaining electronics will go to Point 5*
- *The Tracker has been allocated 5 racks to 'burn in' the power supplies, and 5 racks to commission the FEDs, FECs with the CMS TTC and Trigger Systems in EIC*
- *The use of production cables will ensure that control, voltage drop and timing issues are understood as early as possible*
- *'Cosmic Ray' trigger will give straight tracks to commission the track finding and alignment algorithms*
- *After transport to Point 5 and installation of the Tracker into CMS, the cabling and systematic checks of the System, the commissioning process can continue in CMS*
- *There should be ~ 6 months to continue the commissioning in CMS at Point 5*
- *It is important to have a similar 'cosmic trigger' at both TIF and CMS, so that the commissioning of the High Level Trigger and 'Off-line' software continues without disruption.*
- *The Forward Pixels will use TIF from Q4 2006*