

# The Silicon TPC System

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Jan Timmermans

NIKHEF

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## JRA2 activity/task

- Silicon TPC readout ("SITPC")
  - development MediPix  $\rightarrow$  TimePix chip
  - development diagnostic endplate module incl. DAQ
- Purpose: a SiTPC based monitoring system

Partners:

ALU Freiburg, CEA Saclay, CERN, NIKHEF Associate: Bucarest

## Goals in context of ILC

- Gas multiplication GEM or Micromegas foil(s)
- Charge collection with granularity matching primary ionisation cluster spread (this needs sufficiently low diffusion gas)
- Investigate measurement dE/dx using cluster counting?

- 2D "proof of principle" based on existing Medipix2 readout chip: achieved (2004, 2005) by NIKHEF/Saclay, Freiburg
- Add 3<sup>rd</sup> coordinate: Medipix2  $\rightarrow$  TimePix ( $\rightarrow$ 2006/2007)
- Integrate grid with pixel chip: Ingrid (first results 2005)

### SITPC Tasks:

- Develop the Timepix chip that allows to measure the 3<sup>rd</sup> coordinate (drift time)
- Implementation of Timepix together with GEM and Micromegas into diagnostic endplate system
- Performance measurements in test infrastructure at DESY
- Develop simulation framework
- Develop DAQ system and integrate in overall DAQ of EUDET infrastructure

### Micro Patterned Gaseous Detectors

- High field created by Gas Gain Grids
- Most popular: GEM & Micromegas





readout chip as anode

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### **Results pixel readout gas detectors**



Observation of min. ionising cosmic muons: high spatial resolution + NIM A540 (2005) 295 (physics/0409048) individual cluster counting ! <sup>6</sup>



### JRA2 GEM-TimePix Projekt

Institute of Physics, Albert-Ludwigs-Universität Freiburg

 Micro Pattern Gas Detectors (MPGD): high resolution readout and high rates

• pixelated readout offers high resolution Digital Bubble Chamber

• a TPC at the ILC needs excellent momentum resolution =>GEM-TimePix for end plate readout

• Cluster counting as a goal

Results from MediPix Prototype setup:

For ArCO2 and HeCO2: point/cluster resolution determined with different algorithms between 50 - 60µm



δ-electron in HeCO2





Charge depostion on the MediPix Chip electron track from <sup>106</sup>Ru source and straight line fit



Difference between Micromegas and GEM setup understood (simulation Michael Hauschild)

### Results for point/cluster resolution (from Freiburg GEM+Medipix setup)

	Ar/CO2	He/CO2
3-point method	58 ± 2 μm	53 ± 3 μm
σ <sub>corrected</sub> *	54 µm	53 μm

- with few primary electrons per cluster !
- \* multiple scattering correction to be checked for systematics



### Simulations (M.Hauschild): Threshold Scan



naga 11

TimePix1<sub>(EUDET: Freiburg, Saclay, CERN, NIKHEF)</sub>

•Distribute clock to full 256x256 pixel matrix (50-100-160MHz)

- •Enable counting by first hit after 'shutter' opens, until 'shutter' closes (common stop); also time-over-threshold possible
- •Dynamic range  $2^{14} \times 10 \text{ ns} = 160 \text{ }\mu\text{s}$
- •(for the time being) no zero-suppress to remain fully compatible with Medipix2
- •Shaping time ~200 ns
- •Extra static discharge protection for the could be considered later



- •Keep same chip-size, pixel-size, readout protocol
- •1st full reticle submit done July 2006







- Pvdd=V<sup>2</sup>·f·Ctot
- Simulation @ Vdd=2.2V but digital part could work to 1.8V (33% less power)









100MHz



# Mpix2MXR20 vs Timepix Layout

#### Mpix2MXR20 layout

#### **Timepix layout**







Physical dimensions	14.111 mm × 16.120 mm (like Mpi×2MXR20)	
IO PADs	127 (like Mpix2MXR20)	
Charge collection	e⁻, h⁺	
Pixel functionality	PhotonCounting, TOT, Timepix	
Amplifier Gain	~18mV/Ke⁻	
Noise	~75e <sup>_</sup>	
Linearity	Up to 50Ke⁻	
Thresholds	1 (4bits adj)	
σ equalized	~25e <sup>_</sup>	
Minimum Threshold	~500e <sup>-</sup> (expected)*	
Counter Depth/Overflow	14-bits/Yes	
Max Analog power	6.5µW/pix 420mW/chip (@2.2V Vdda)	
Static Digital Power	440mW@100MHz (@2.2V Vdd)	
Readout	Serial/Parallel	
Readout compatibility	95% (Clock active when shutter ON)	



# Timepix: Next Steps

- Expected 12 wafers out of foundry by 19<sup>th</sup> of September  $\Rightarrow$  25<sup>th</sup> to 30<sup>th</sup> September delivered at CERN
- Testing of the chip on wafer using the Medipix2 probecard in the Silicon Lab clean room
- First version of Timepix firmware for the Muros2.1 readout is already prepared
- 1-2 month of chip characterization and wafer probing expected









- MUROS arrived in Saclay
- Upgraded to the latest firmware of the Medipix2 USB Interface with a home-made cable : USB1.1 v1.0.6 -> USB1.1 v1.0.7
- Design of a small drift chamber for the chip
- Gain measurements using different gas mixtures with a Micromegas detector



#### Gain measurements





saclay



- •<u>Aim</u> : find a gas with a comfortable gain margin
- Using the same detector
- Transparent plastic box of 25cm x 25cm x 5cm size
- Sources :
- Fe 55 (5.9 keV)
- COOL-X (8.1 keV)
- Student : Michal Was (ENS)

#### Gain measurements : results

dapnia







#### Gain measurements : results

dapnia





#### Gain measurements : comparison with simulations







#### Gain measurements : comparison with simulations





dapnia



## NIKHEF/Twente: InGrid (Integrated Grid)



### Any field structure feasible



## Gain for different gap sizes

+ many other measurements (homogeneity, gain vs. hole size, energy resolution)



# Sparking

- Chip faces 80kV/cm with no protection (unlike the GEM setup)
- Degradation of the field, or total destruction of grid but also CMOS chip



10µm

## Solution: high resistive layer

- Resistive plate chamber principle
- Amorphous silicon deposited
  - 4um thickness, resistivity ~ $10^{11}\Omega$ .cm, temperature ~250 °C
- 2 fields with Micromegas: one protected, one unprotected
- First signals recorded; they look the same





### First Twingrid's



### **Further Developments**

RELAXD project (Dutch/Belgian) NIKHEF,Panalytical,IMEC,Canberra:

- Chip tiling: large(r) detector surfaces (2x2, 2x4 chips)
- Through Si connectivity: avoiding bonding wires
- Fast readout technology (~5 Gb/s)



## In summary: timetable

- 1<sup>st</sup> version Timepix operational: 1/2007
- First m.i.p. signals with Timepix: ~4/2007
- Development 2<sup>nd</sup> iteration Timepix during 2007
- Endplate infrastructure: 1/2008
- Full SITPC infrastructure incl. DAQ available: 1/2009

